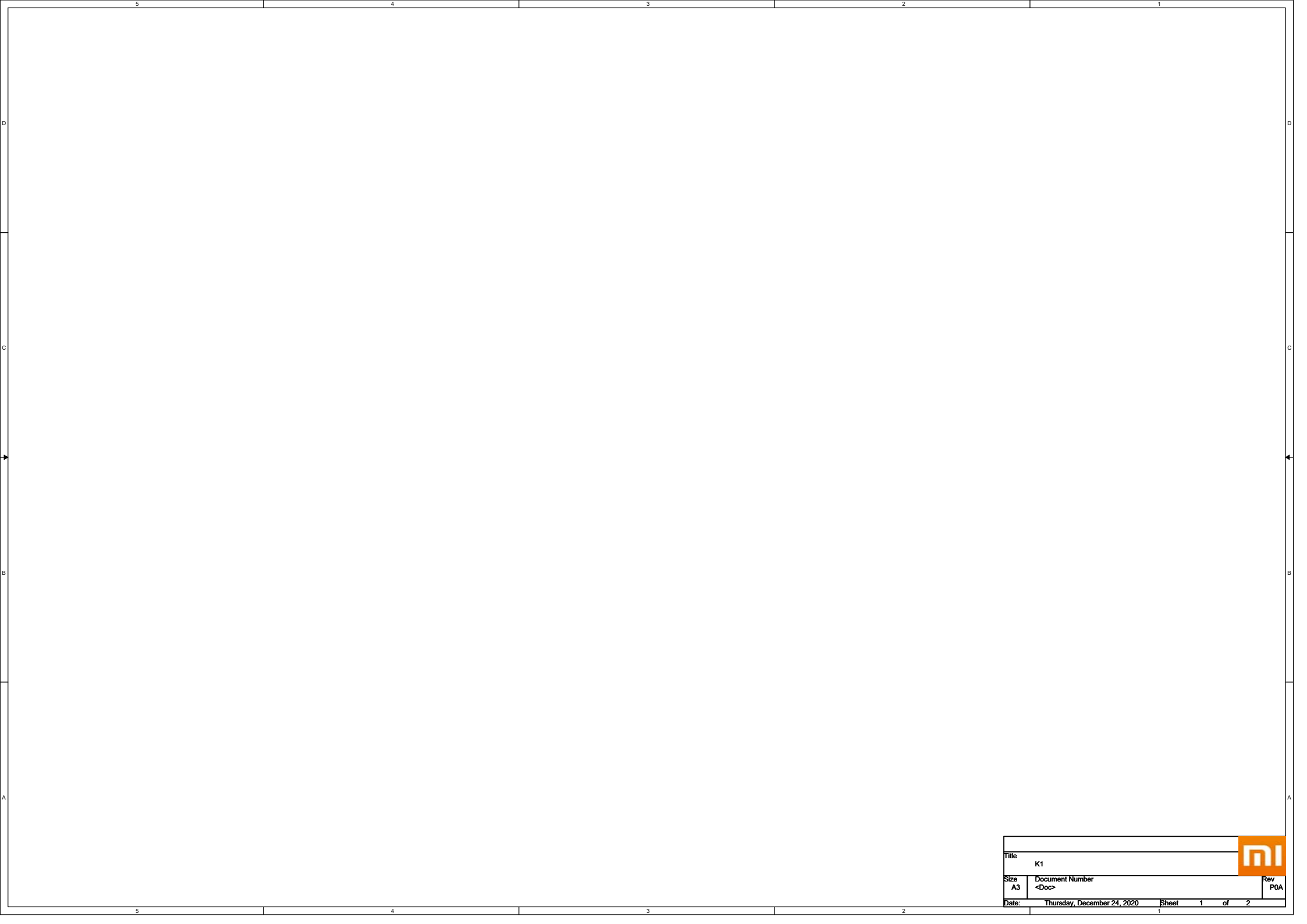




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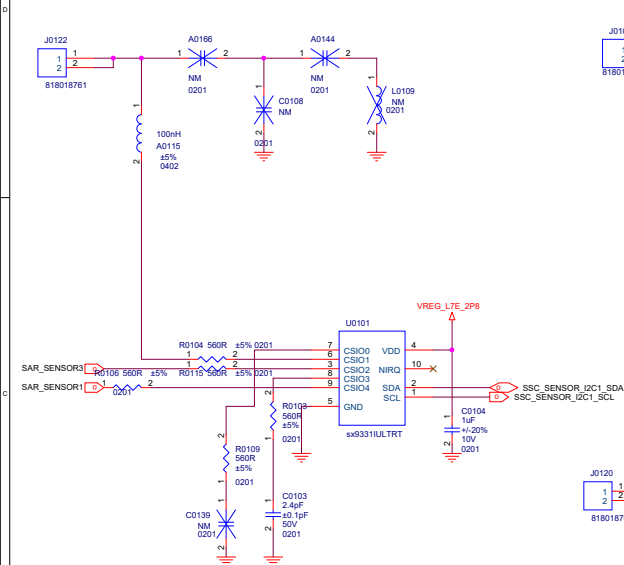
A

A

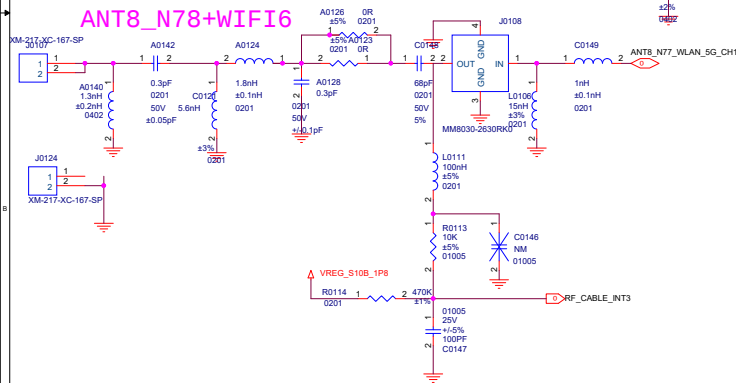
Title		K1						
Size	A3	Document Number	<Doc>					
Date:	Thursday, December 24, 2020		Sheet	1	of	2	Rev	P0A

ANT14 INTERPOSER

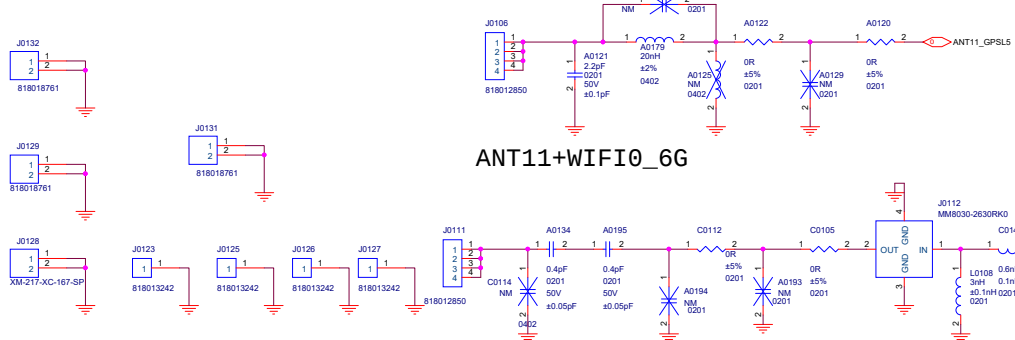
SAR sensor



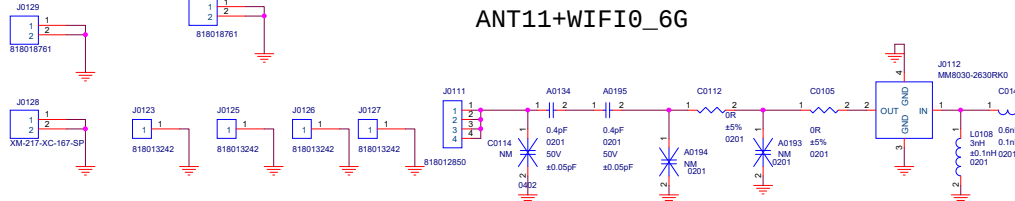
ANT8_N78+WIFI6



ANT11_GPSL5

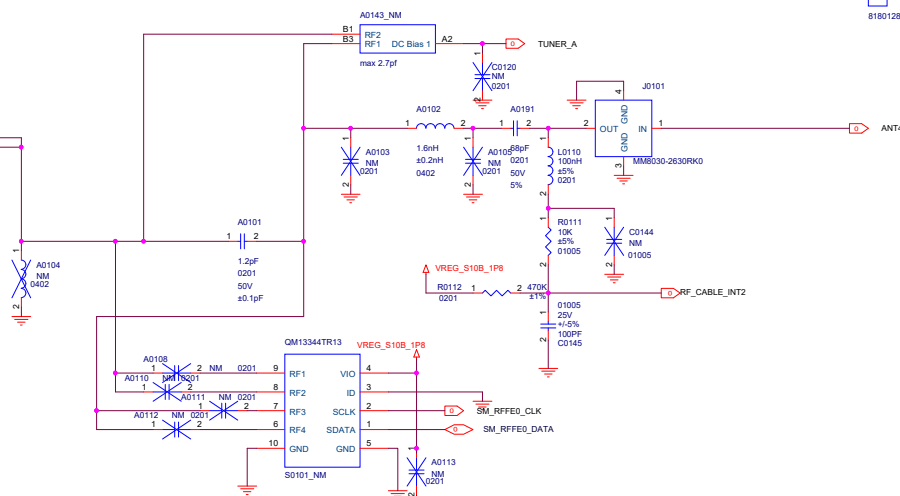


ANT11+WIFI0_6G

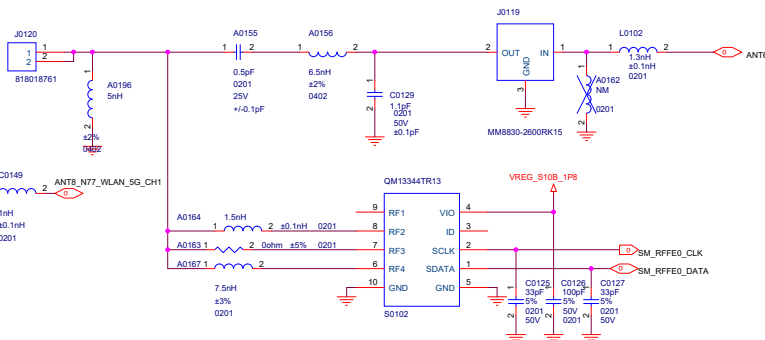


ANT_DIV

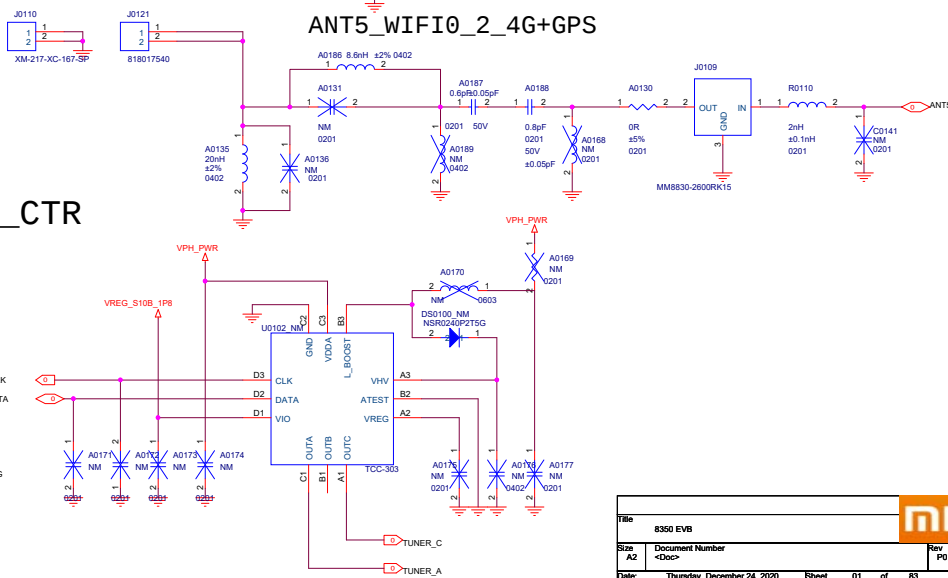
ANT4



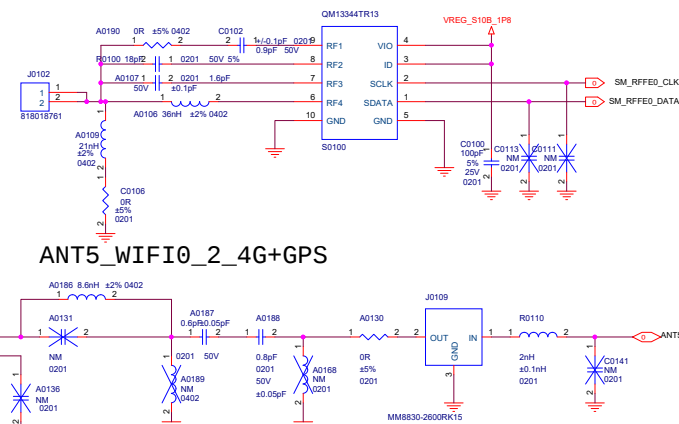
ANT6



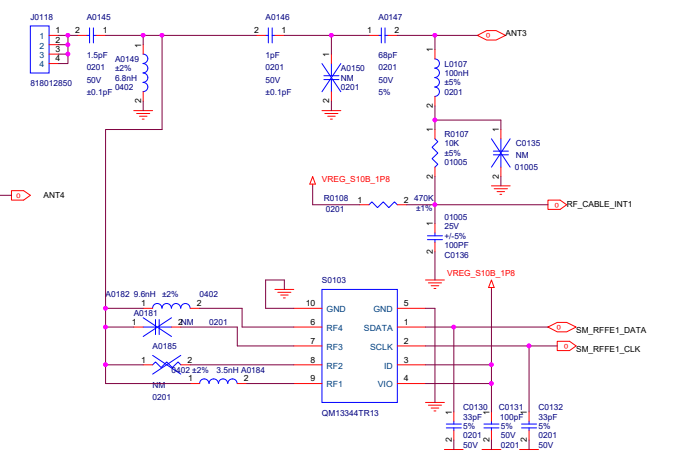
TUNER_CTR

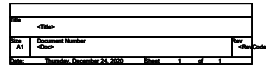


ANT5 WIFI0 2 4G+GPS



ANT3



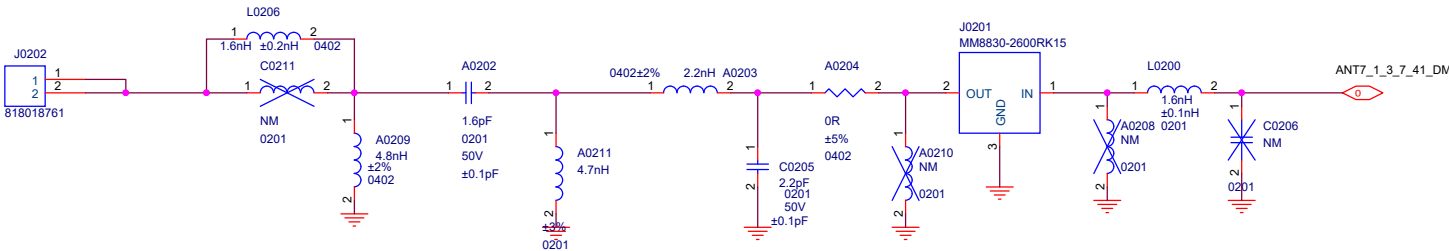


ANT13_B32

ANT12 INTERPOSER

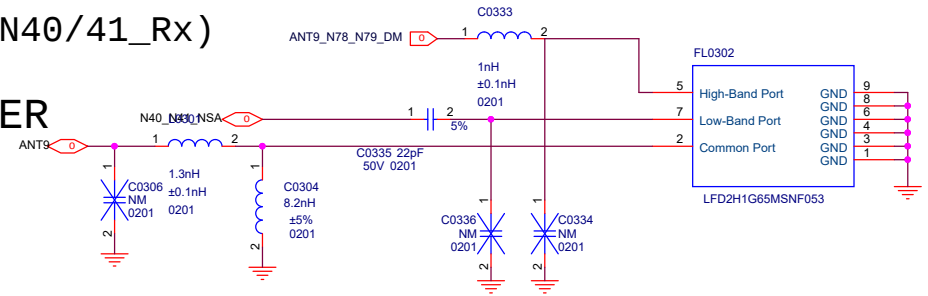


ANT7_1/3/7

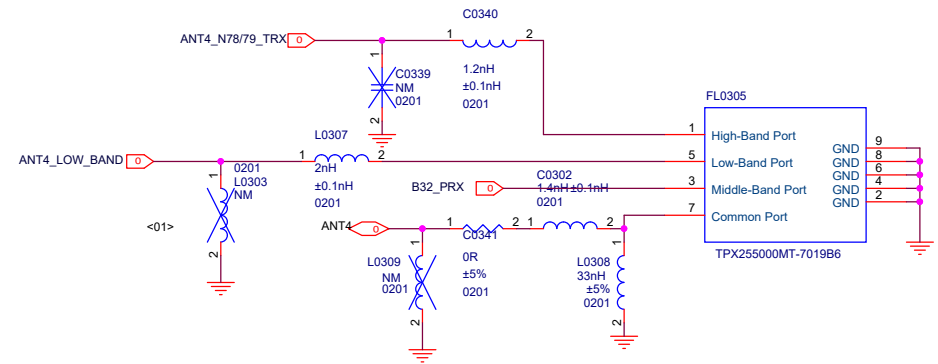


ANT9(N78/79+N40/41_Rx)

INTERPOSER



ANT1(LB)

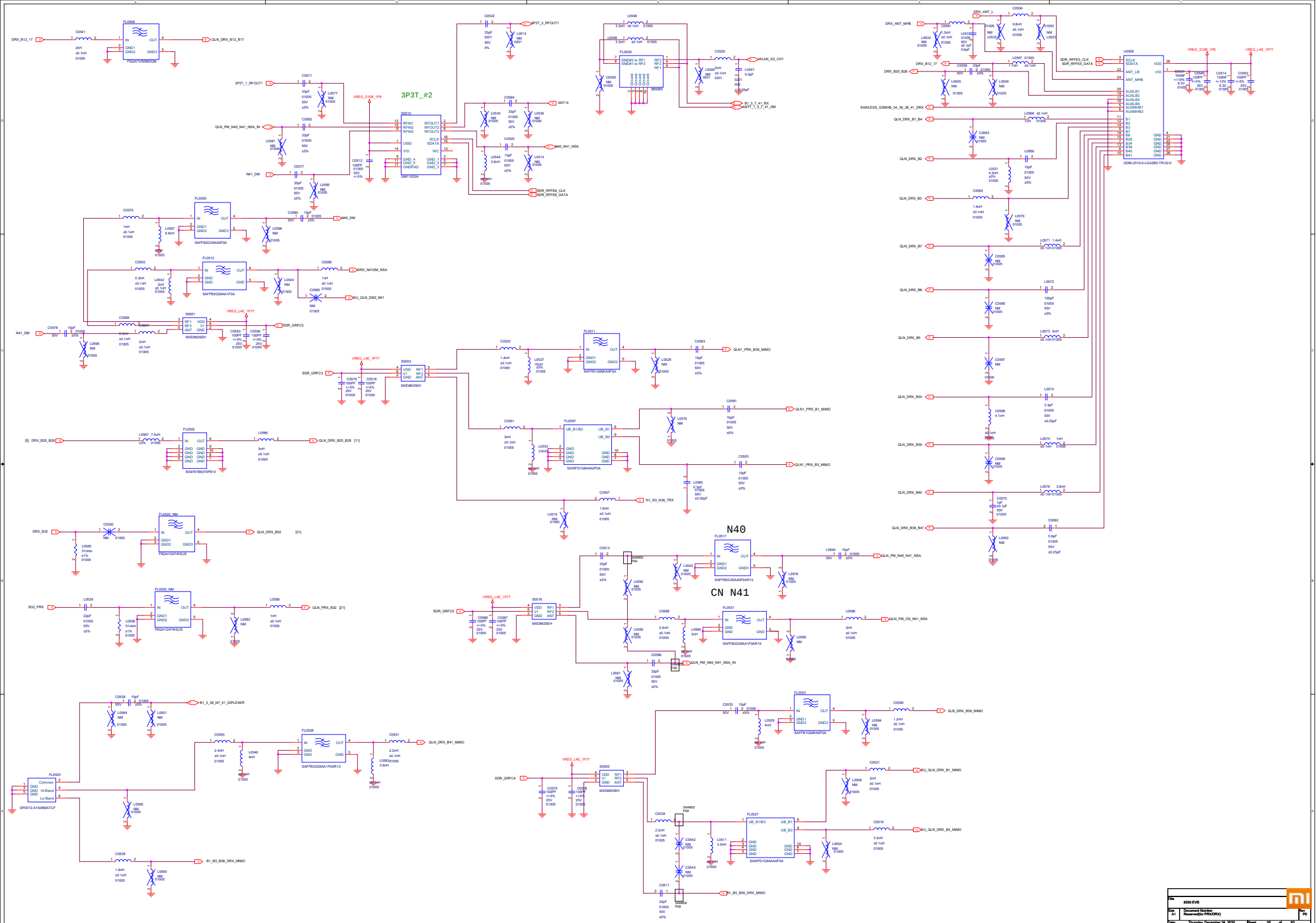


ANT7_N1/N3/N7(PAGE05) ANT14_N78+N79 (PAGE20)

ANT4_MHB(PAGE21) ANT15_B32_DRx (PAGE05)

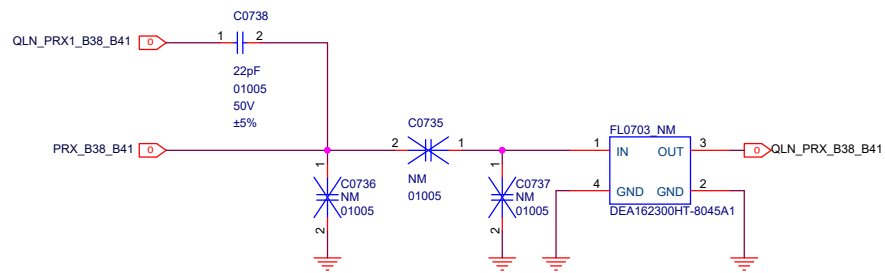
ANT12(N78+N41)

ANT13(N41+N79)



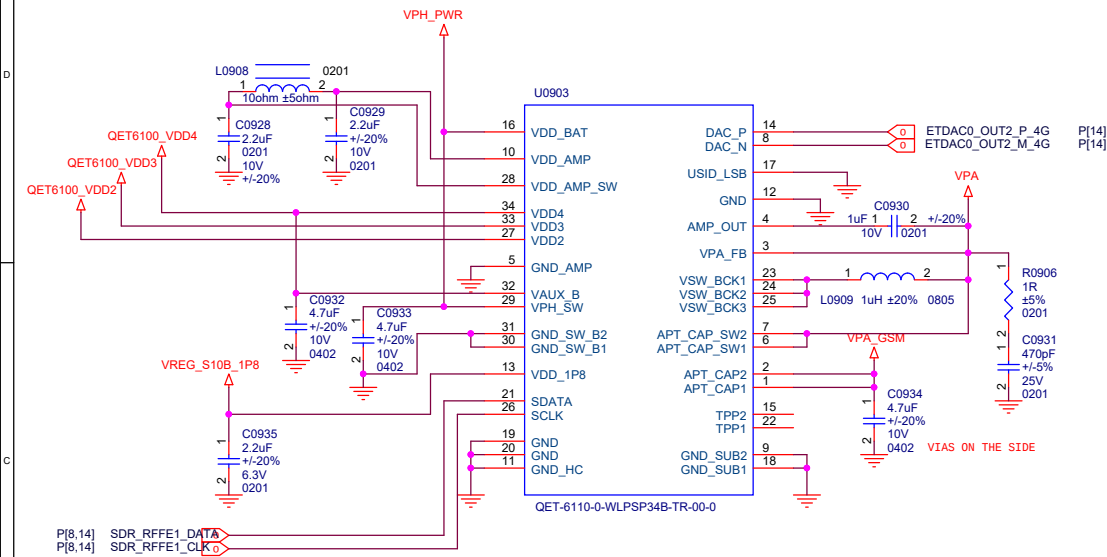
LB S-PAD INTERPOSER



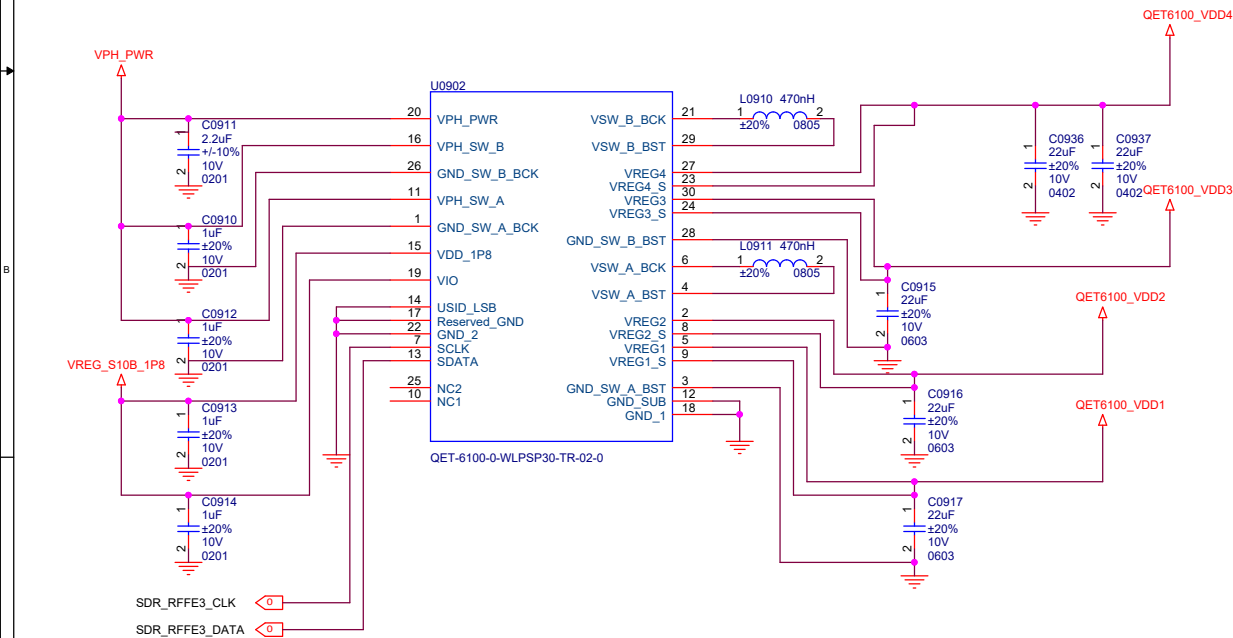
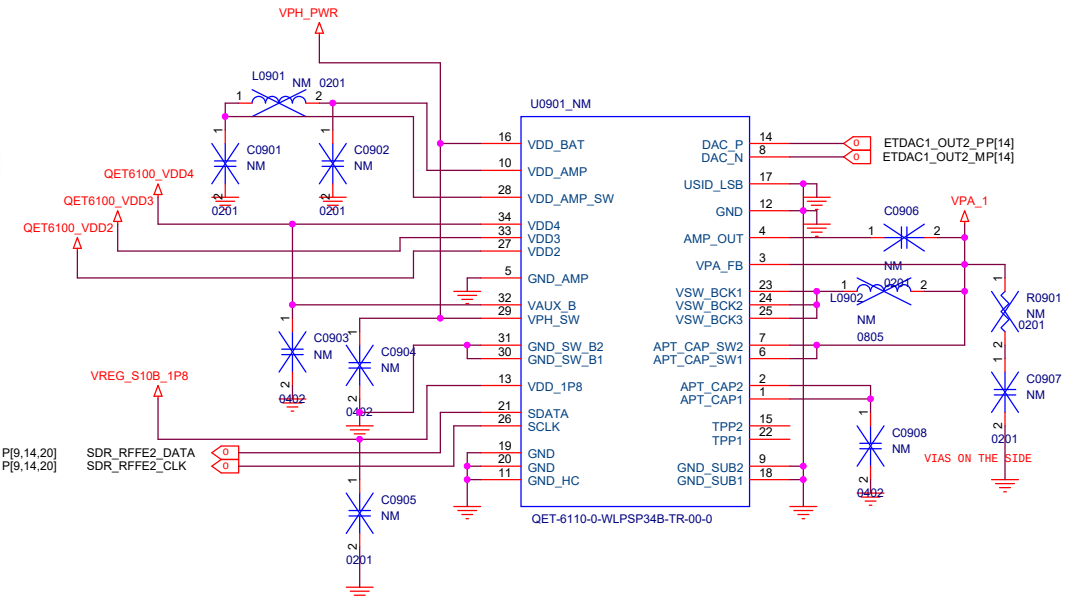


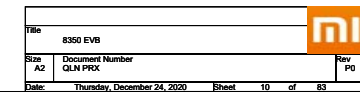
INTERPOSER

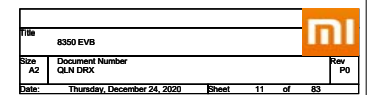
DC FOR 4G

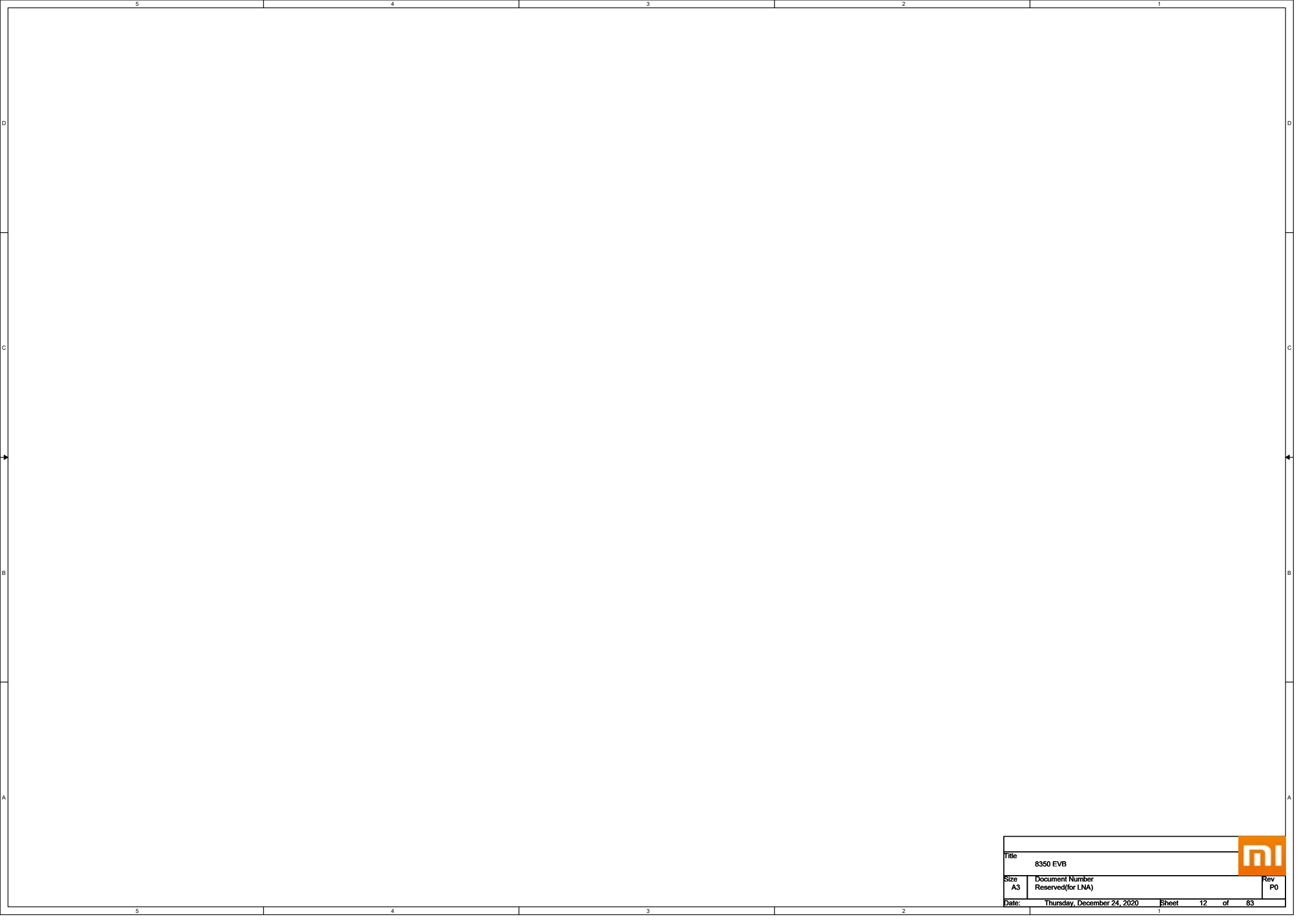


DC FOR QPA5581

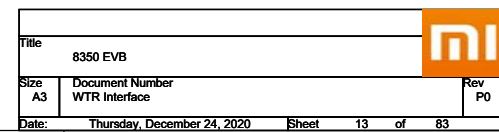
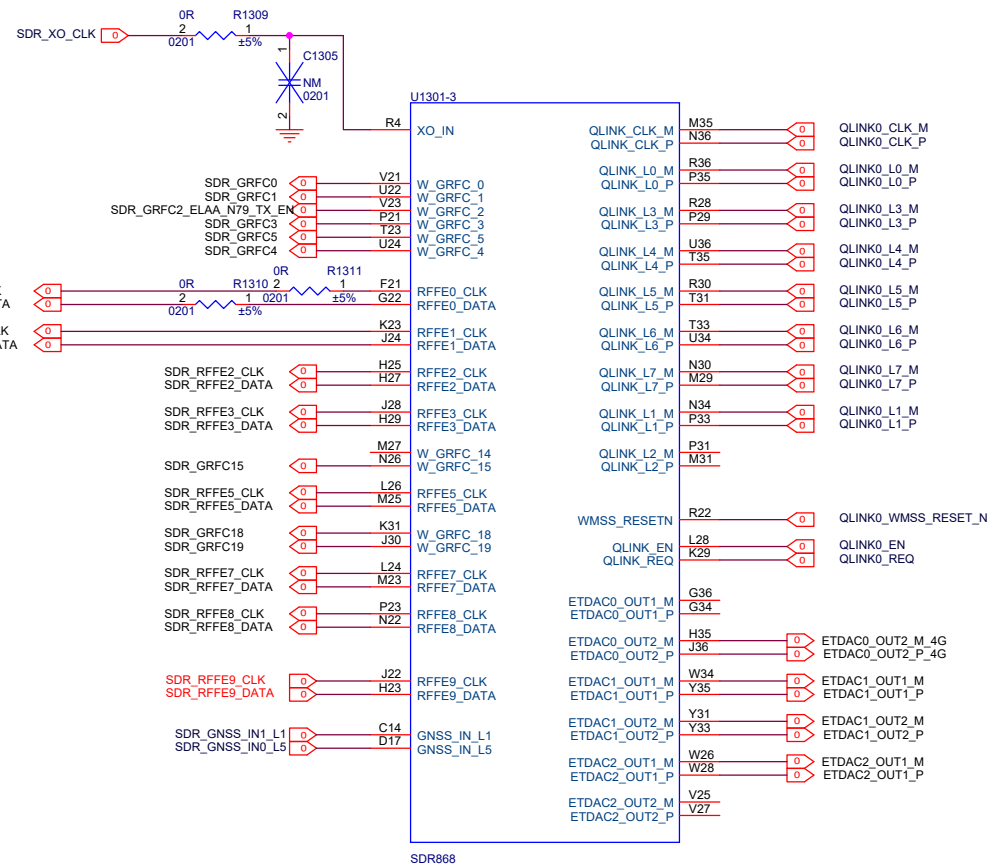


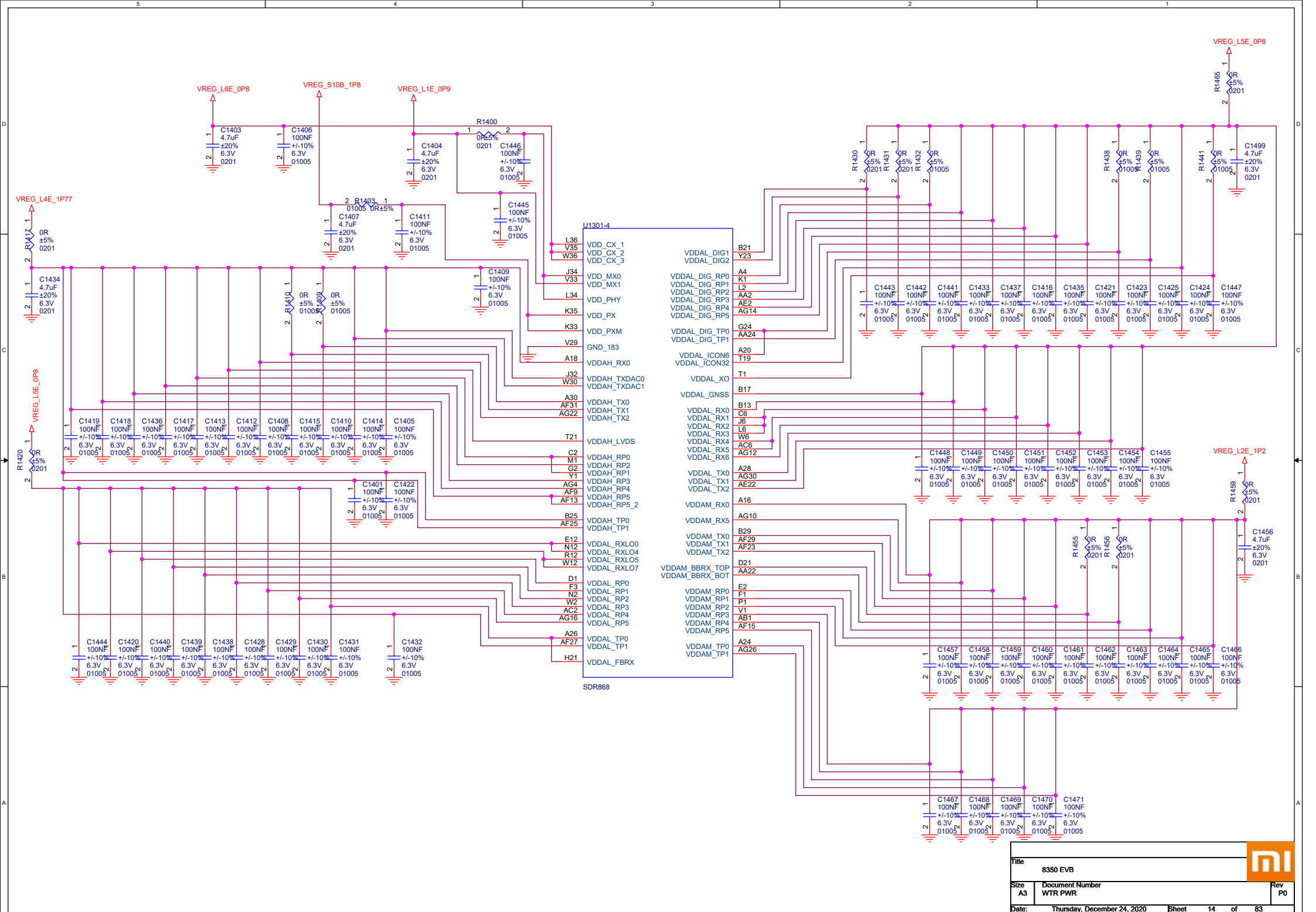


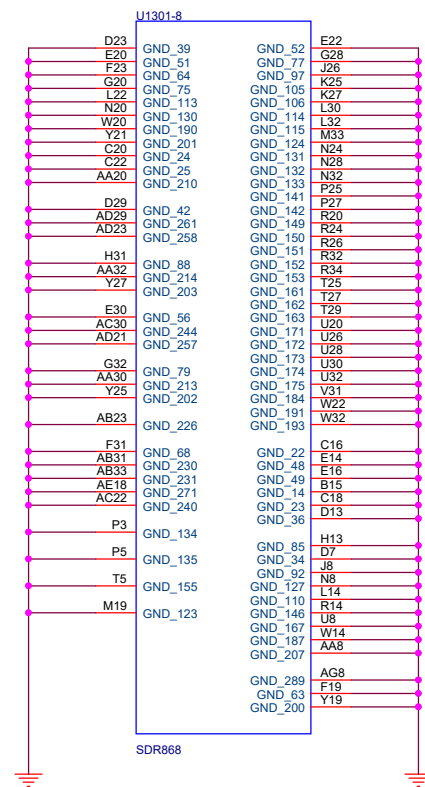
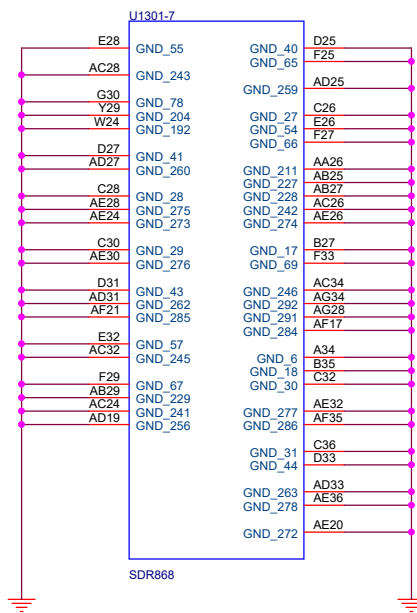
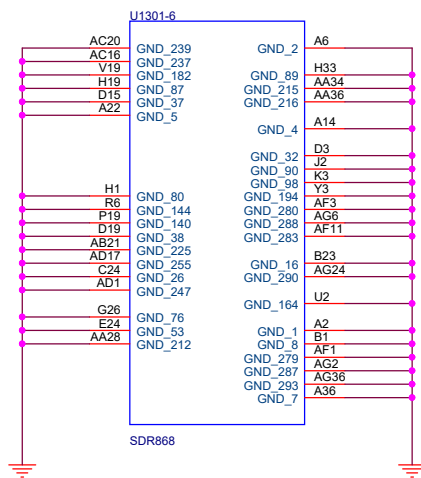


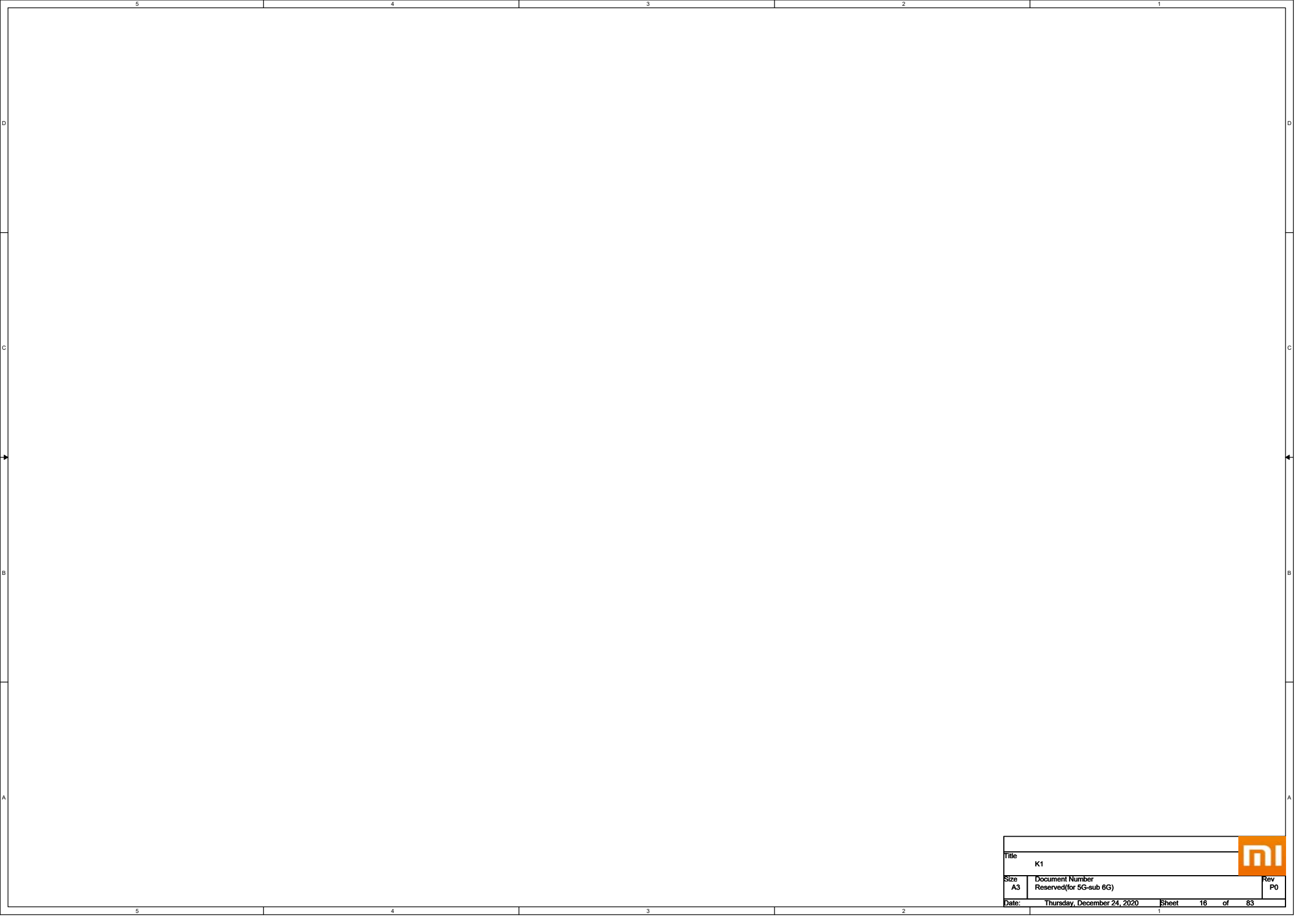


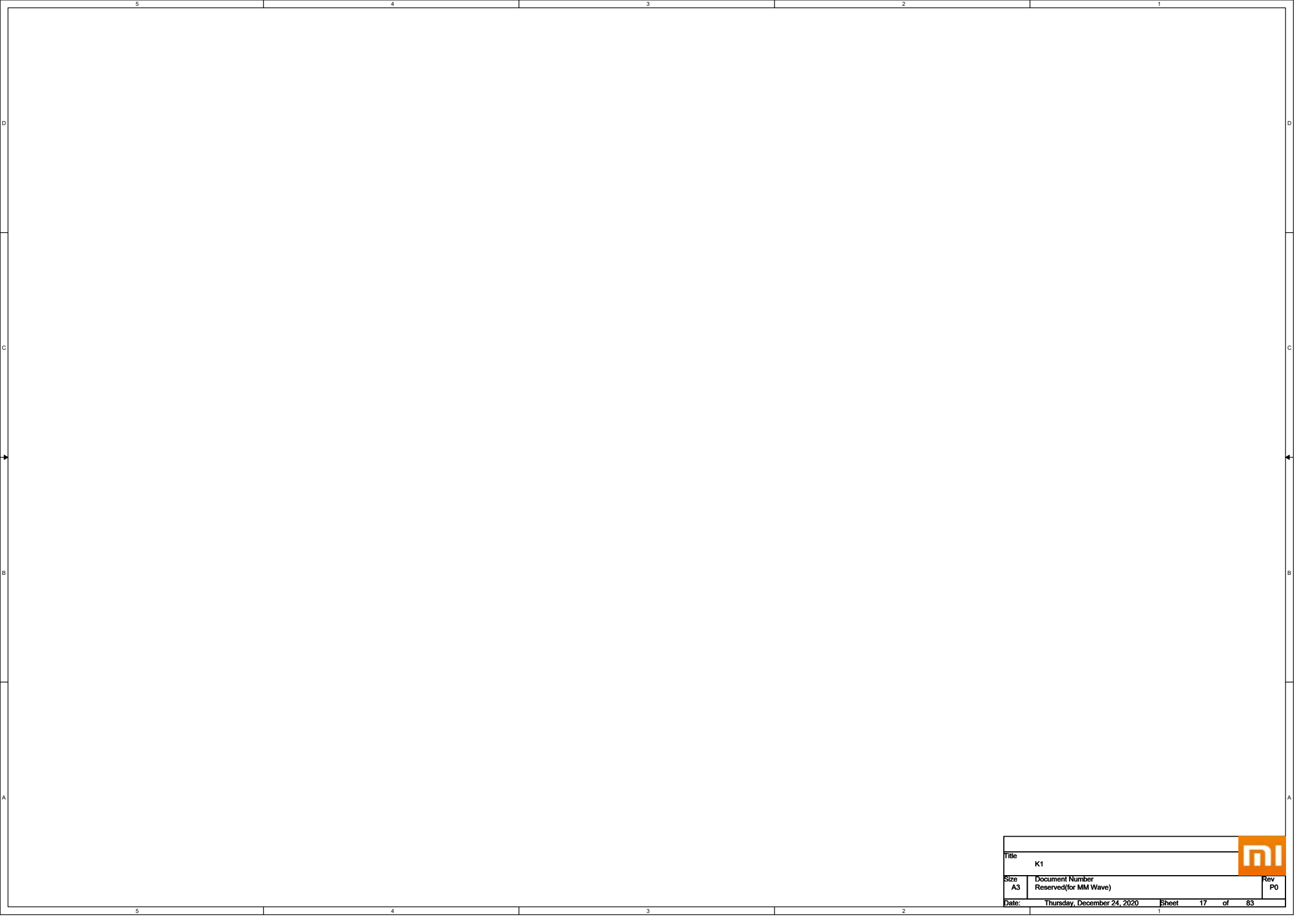
Title				
8350 EVB				
Size	Document Number Reserved(for LNA)			Rev
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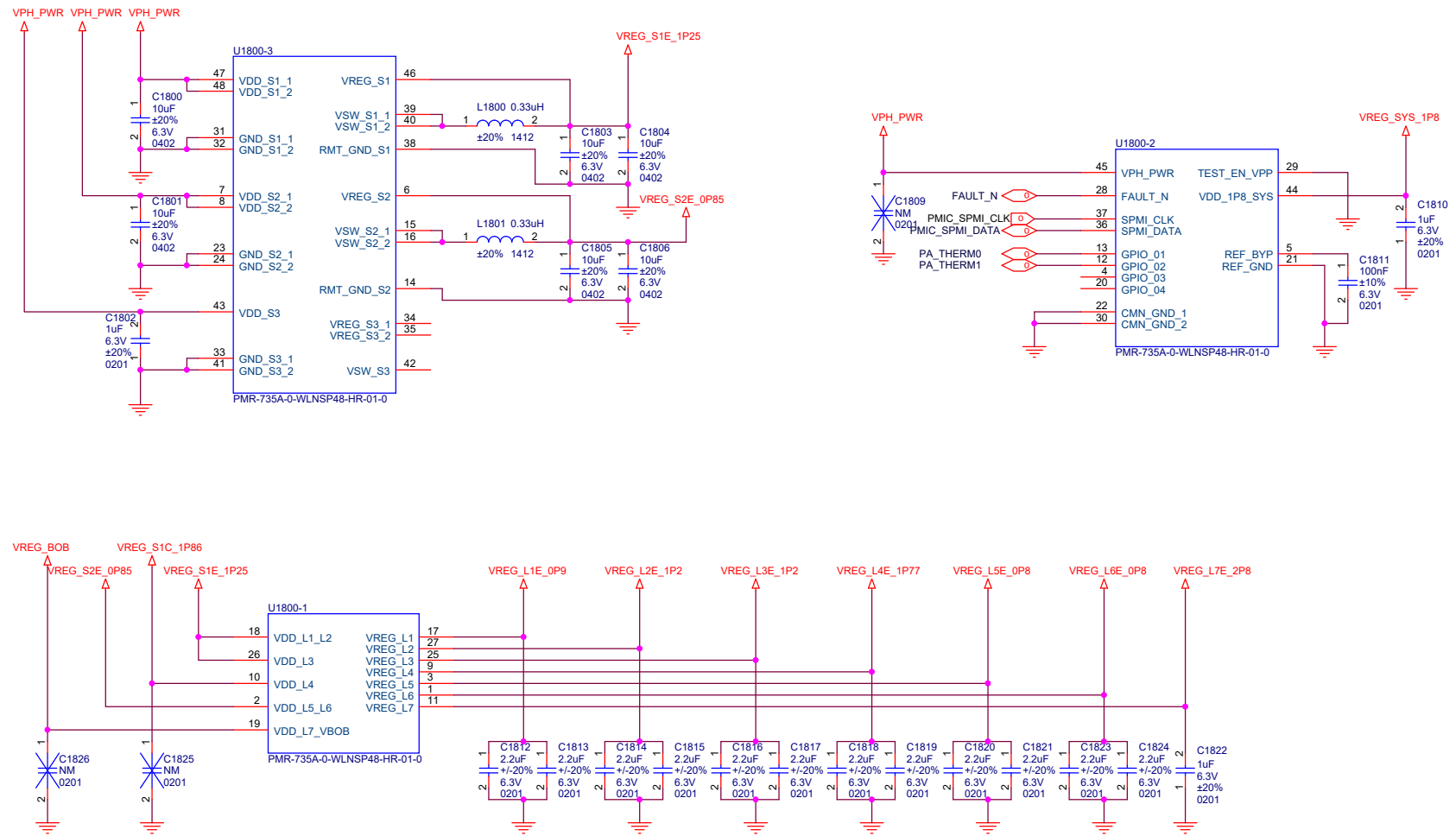


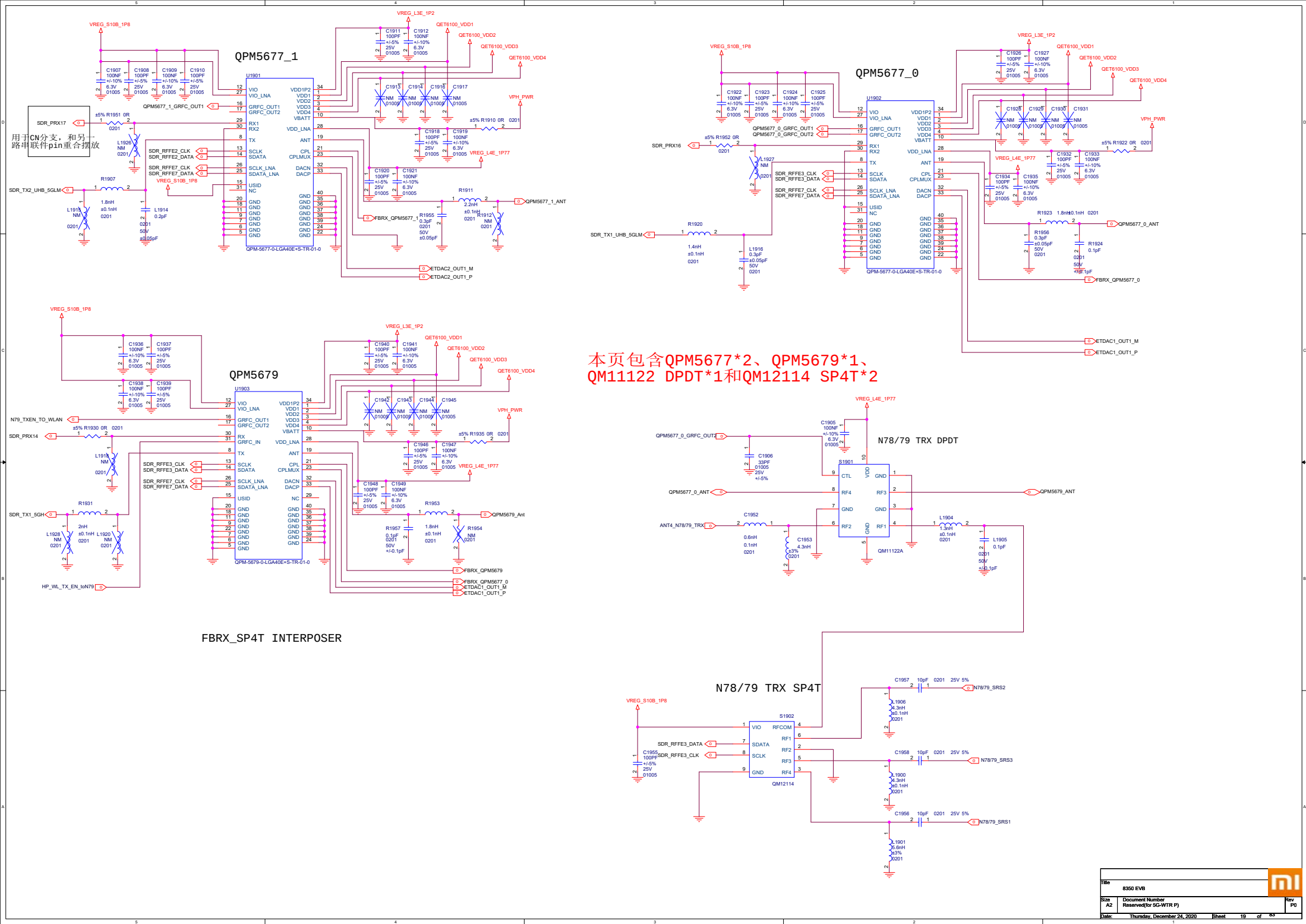


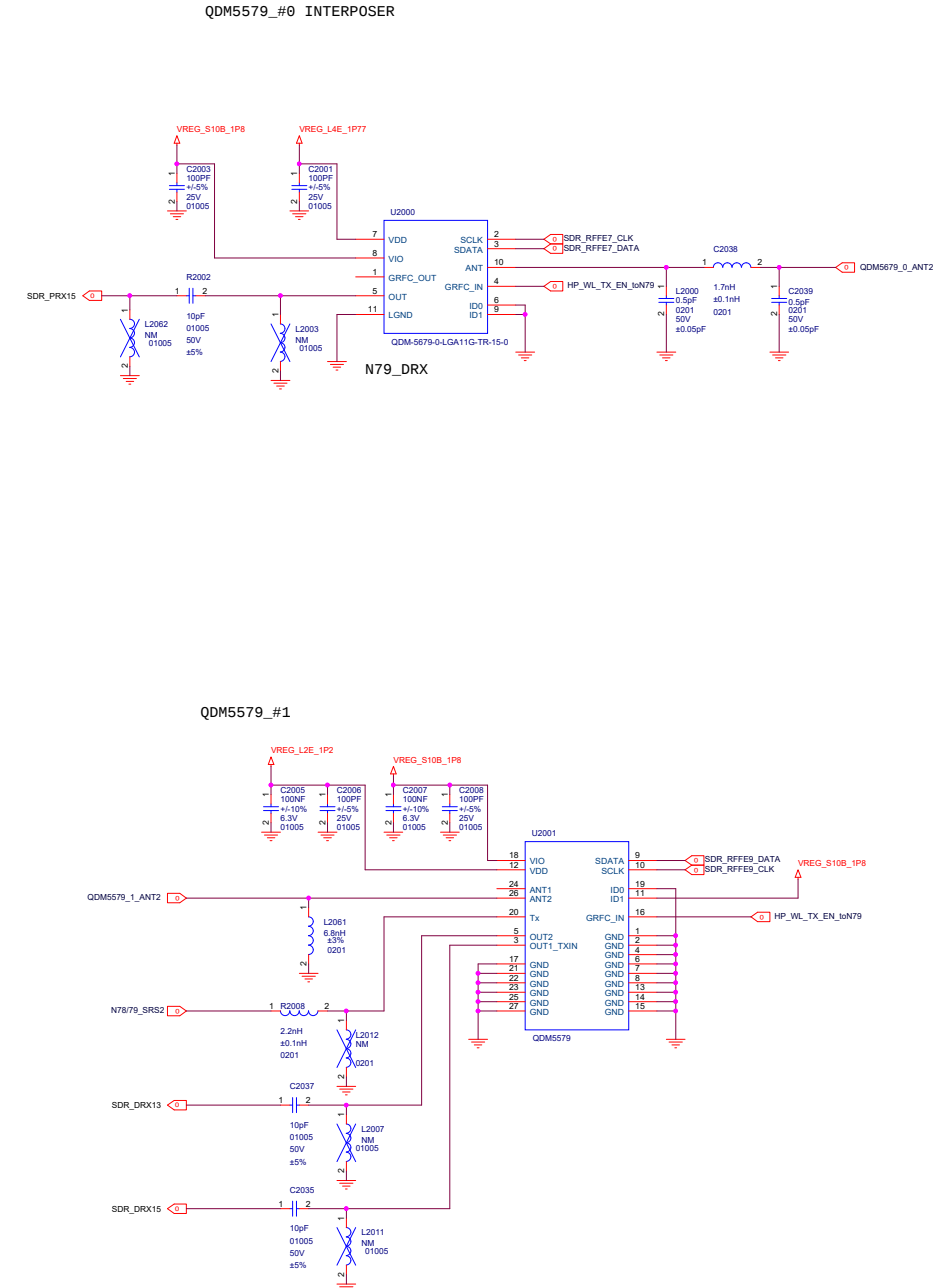
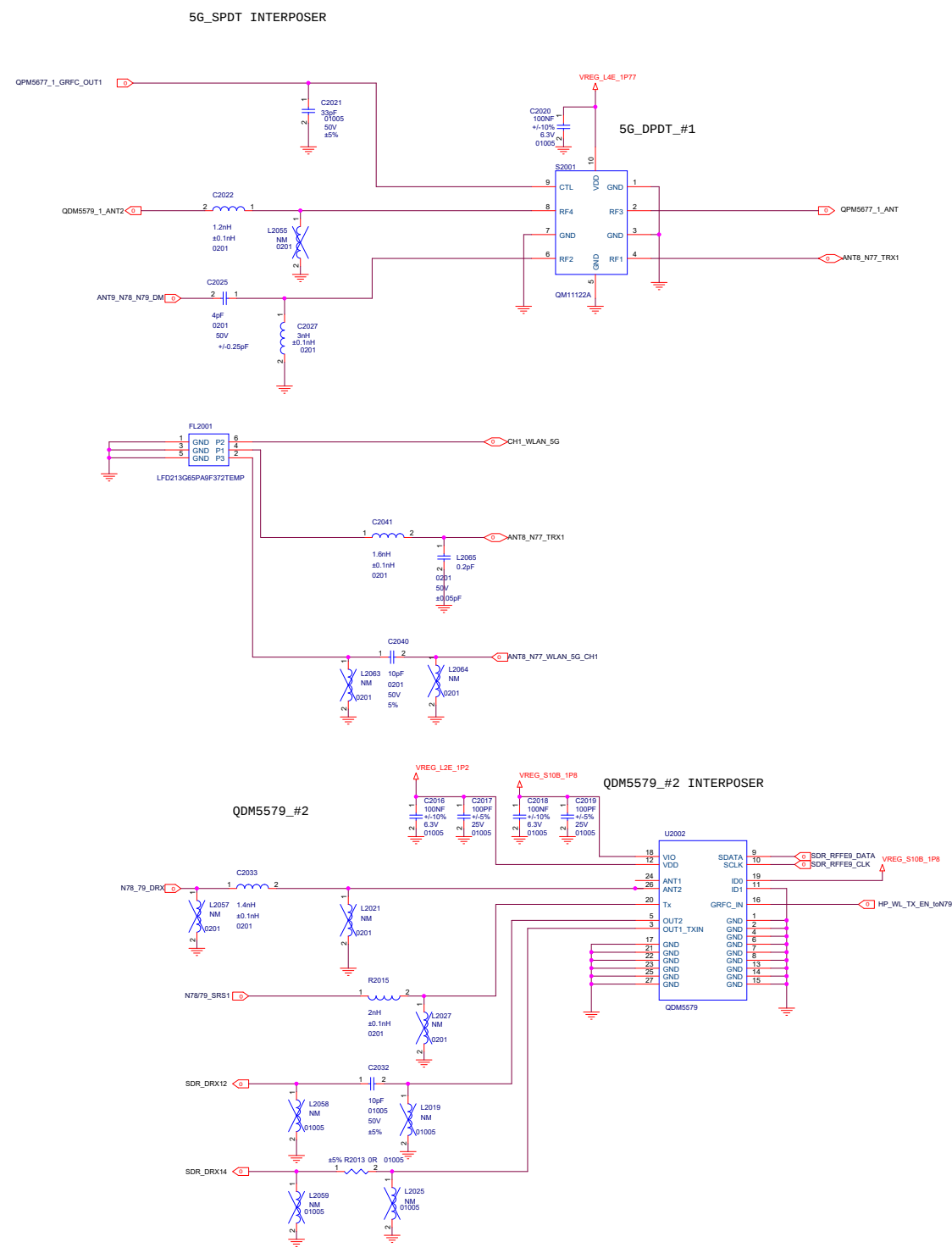




			
Title K1			
Size A3	Document Number Reserved(for MM Wave)	Rev P0	
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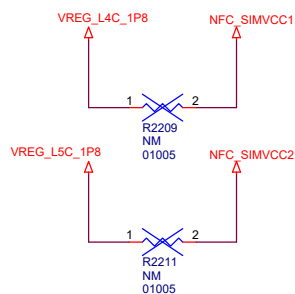


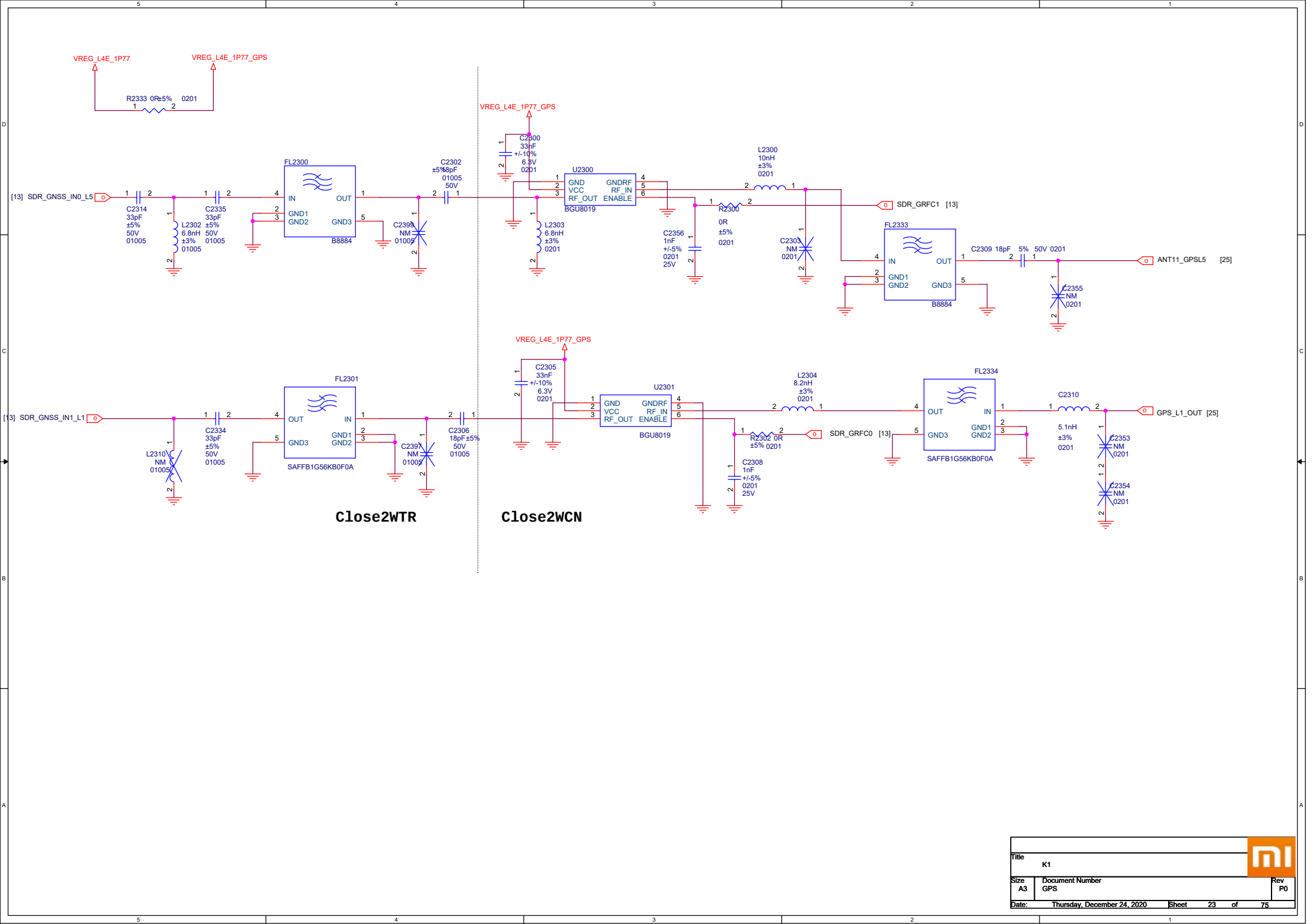
QPM5641

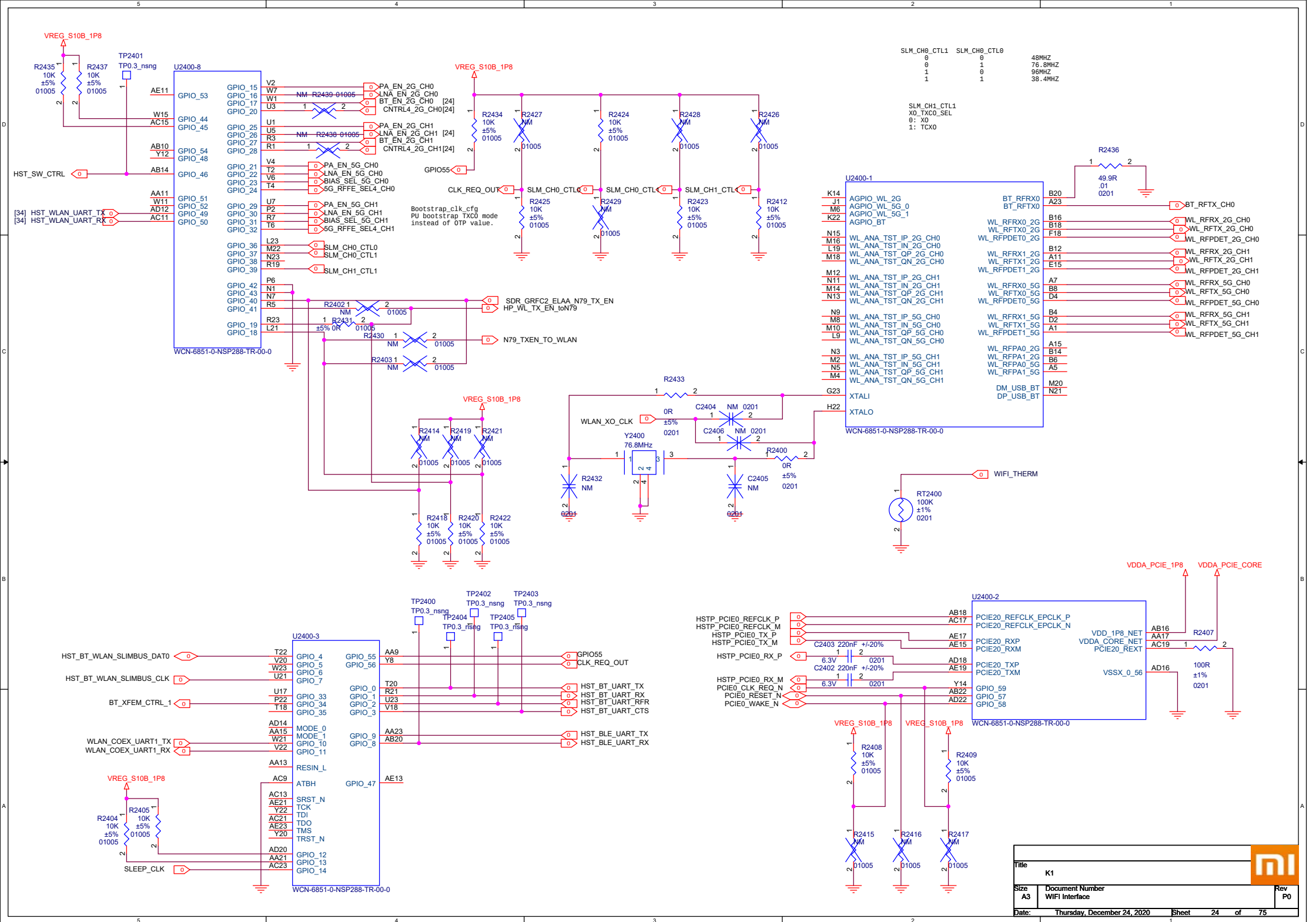
INTERPOSER

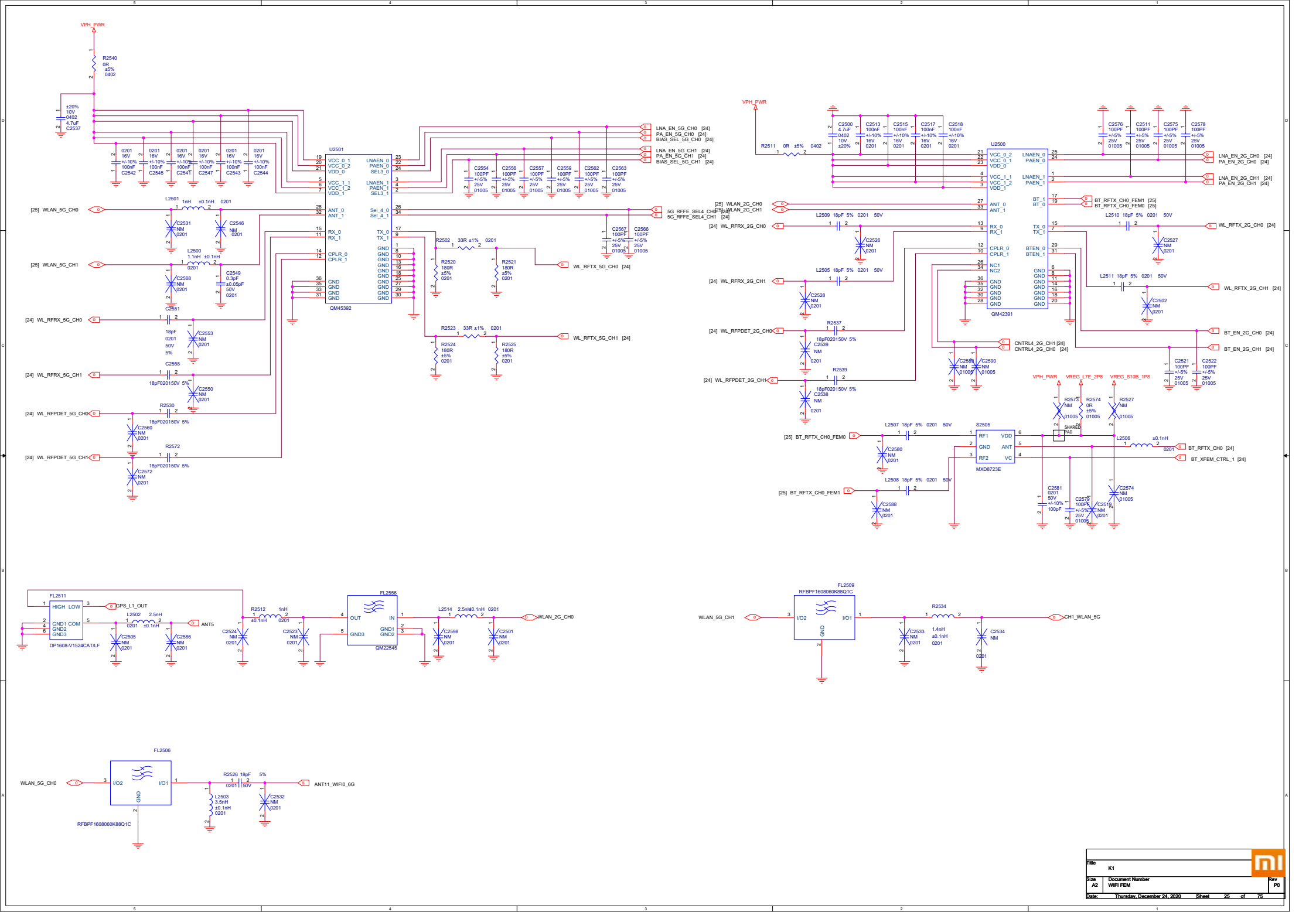
本页包含QPM5641到Ant4的网络，并兼容QPM6585

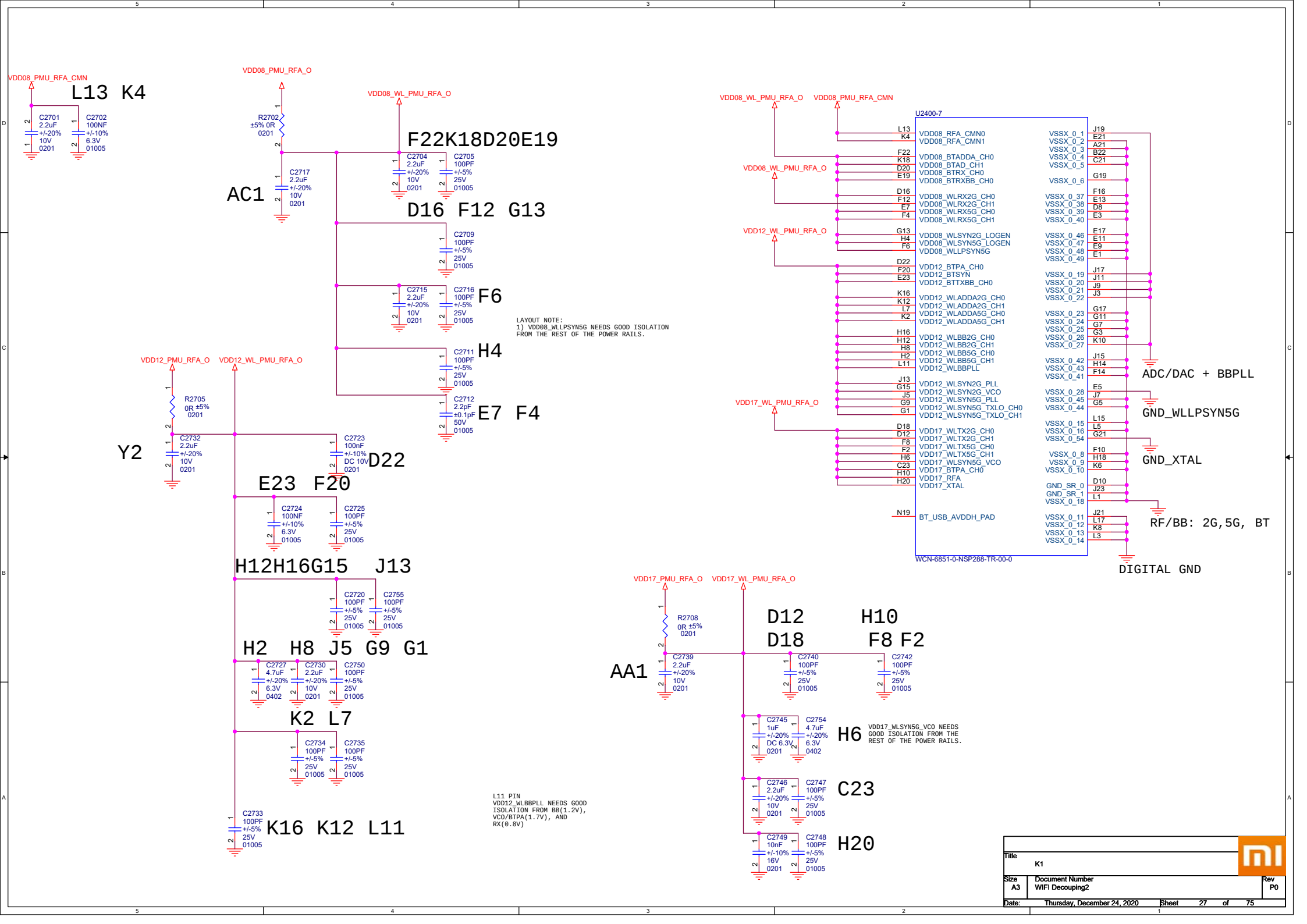
3P3T_1, IN口
为临近天线端

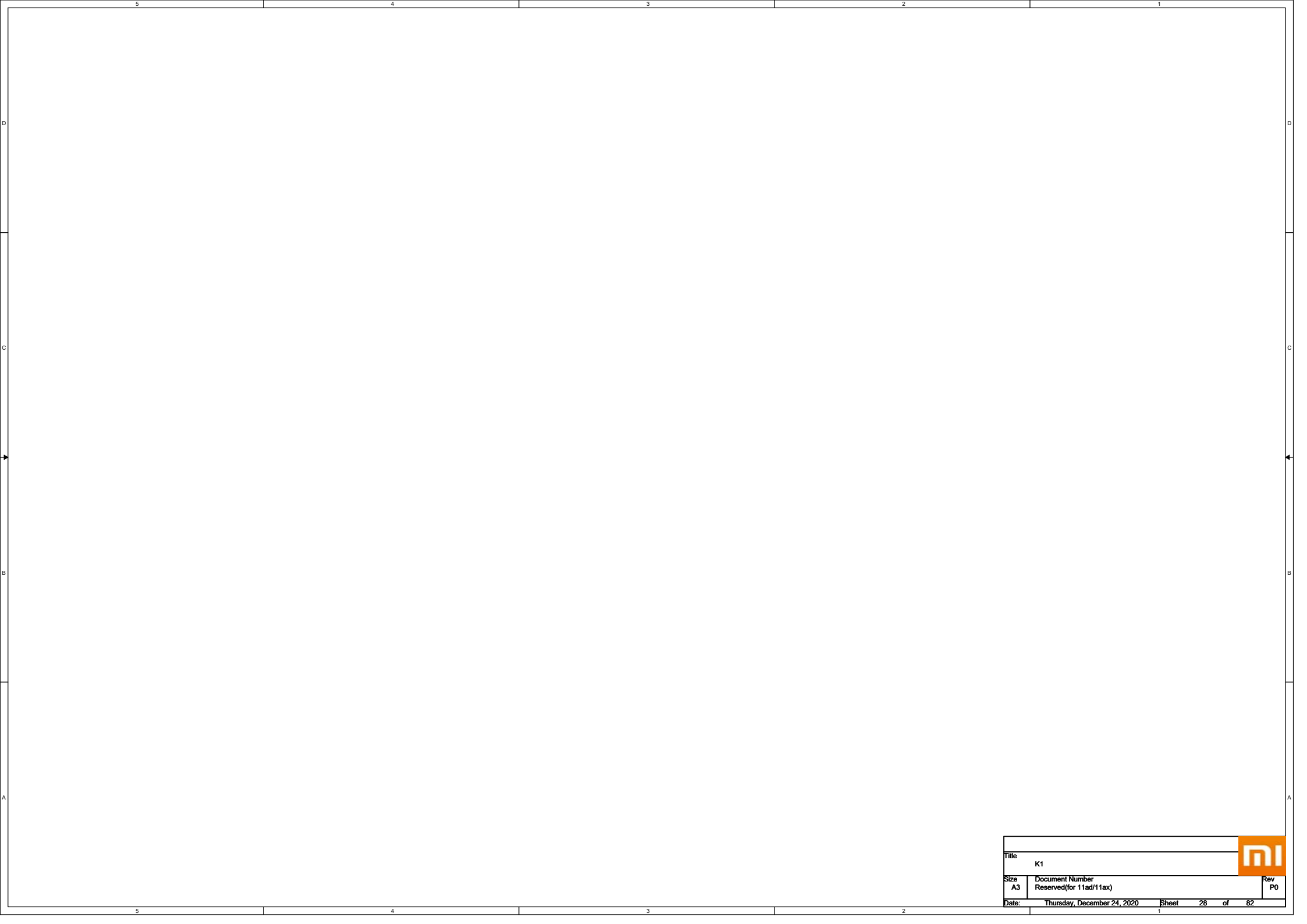




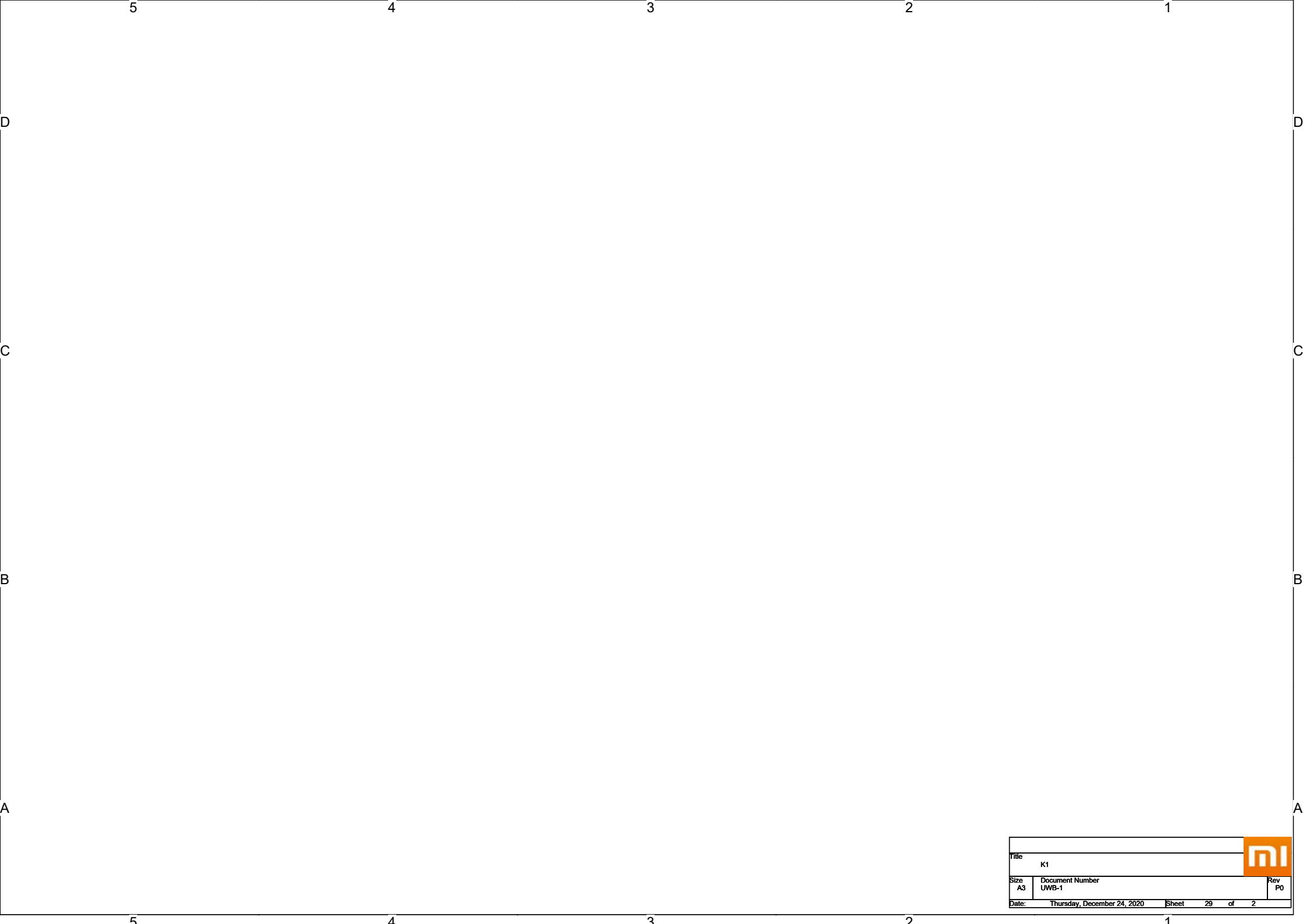


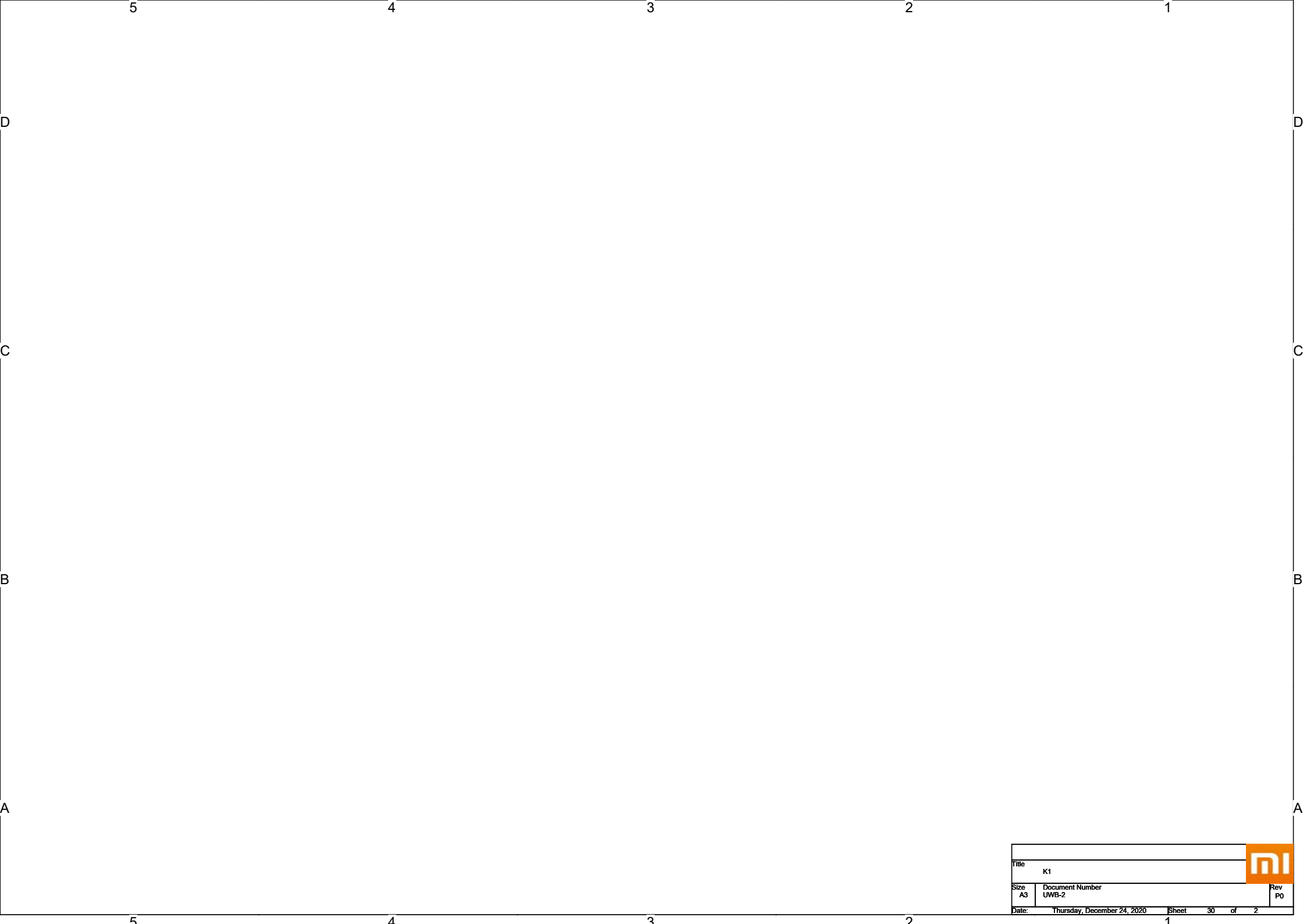




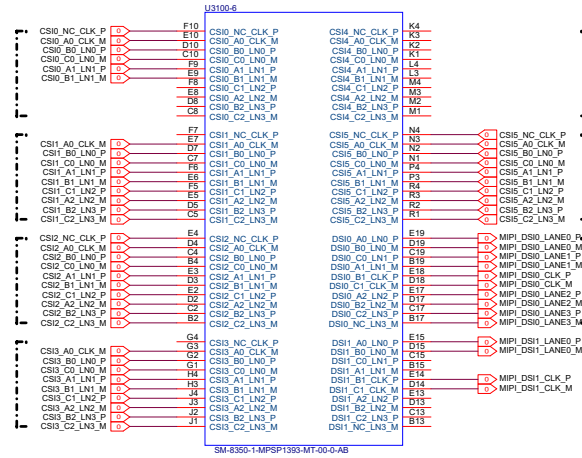


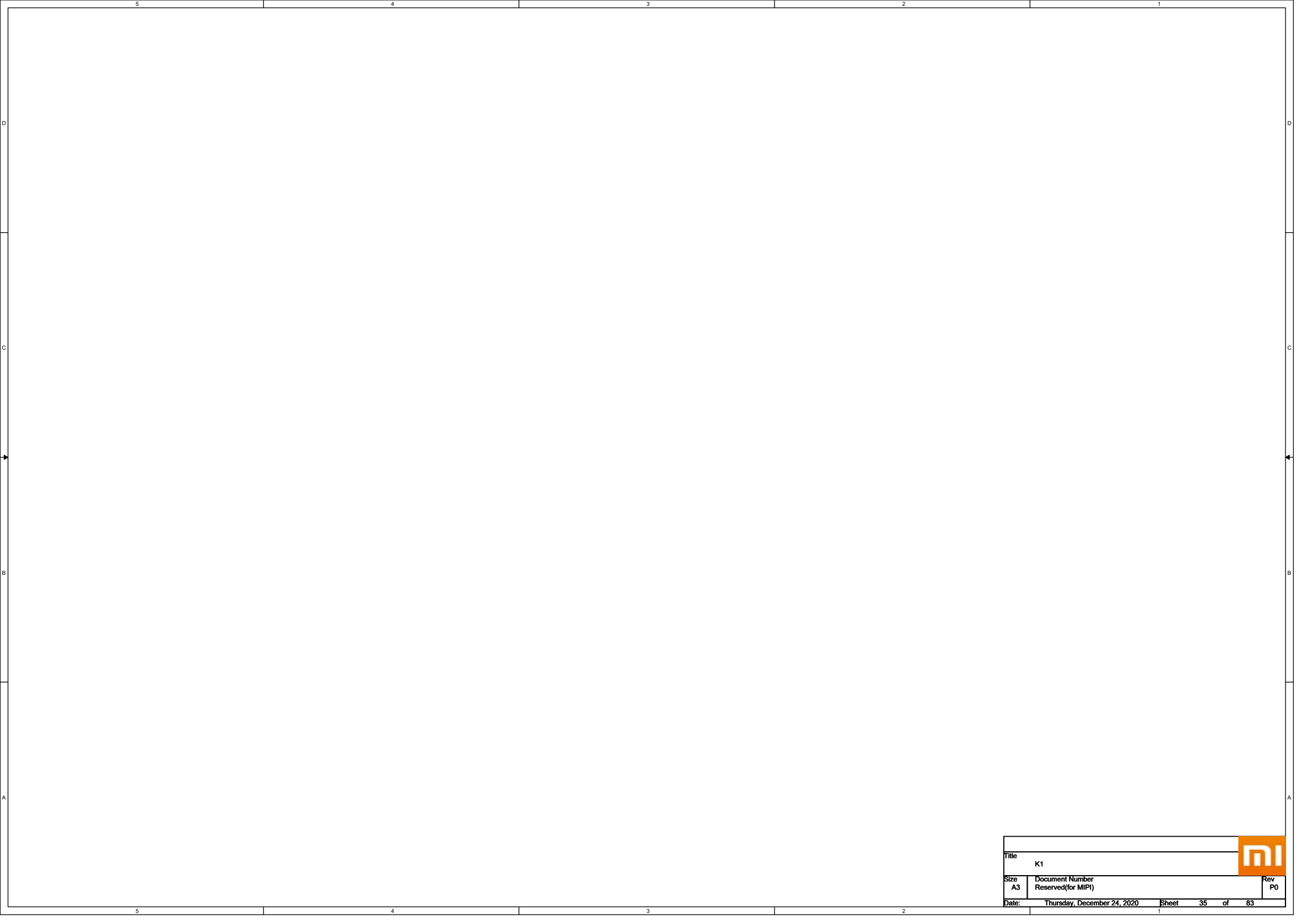
Title		
K1		
Size	Document Number	Rev
A3	Reserved(for 11ad/11ax)	P0
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Title		K1			
Size	A3	Document Number	UWB-2		
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				Rev	P0





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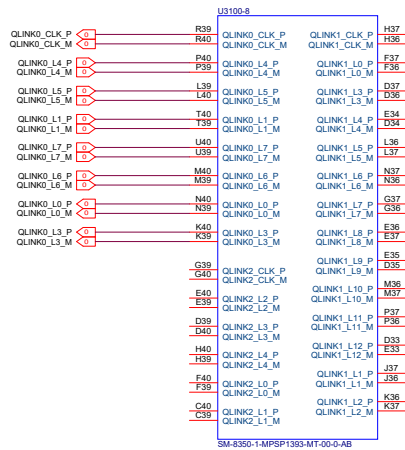
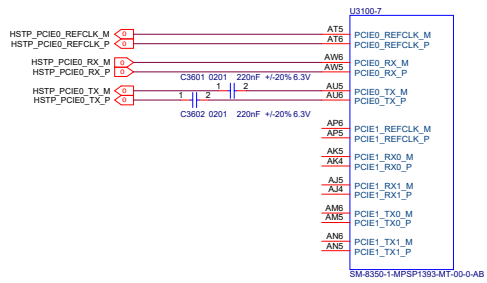
D

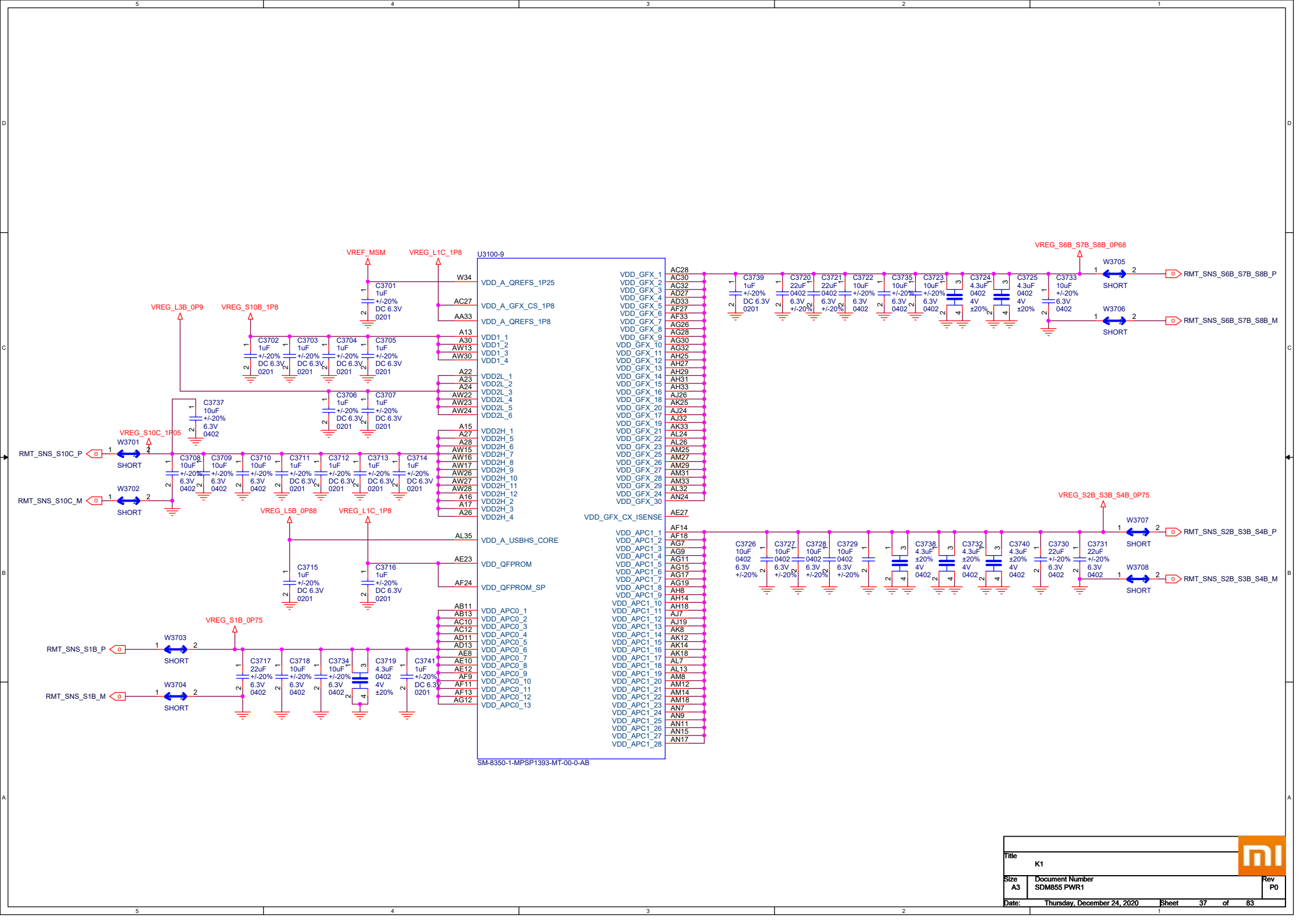
C

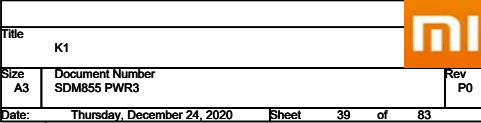
B

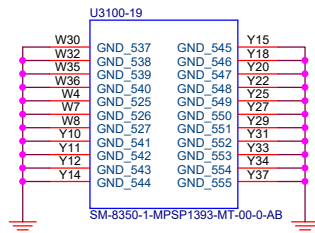
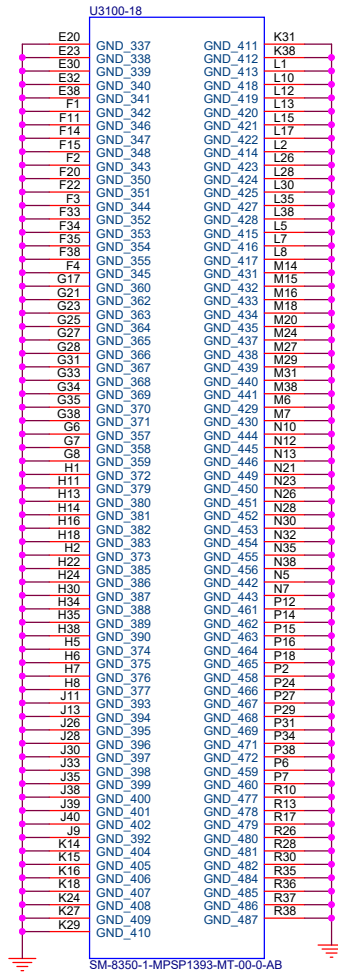
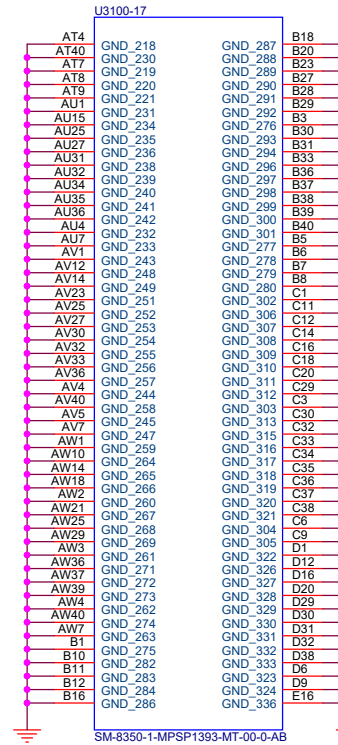
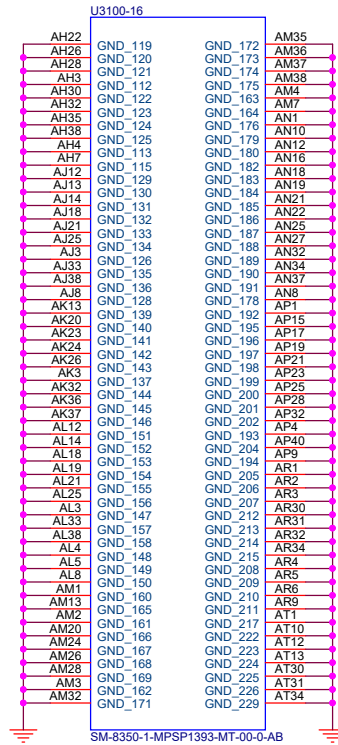
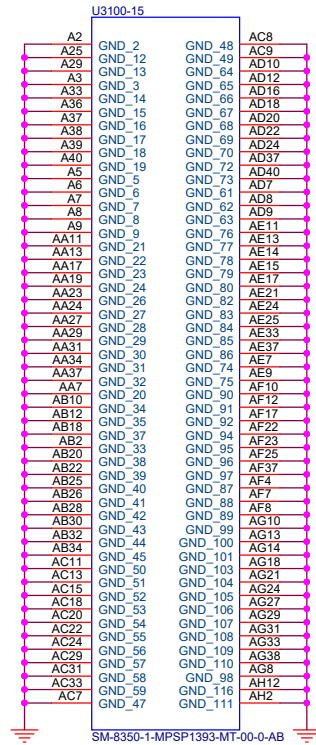
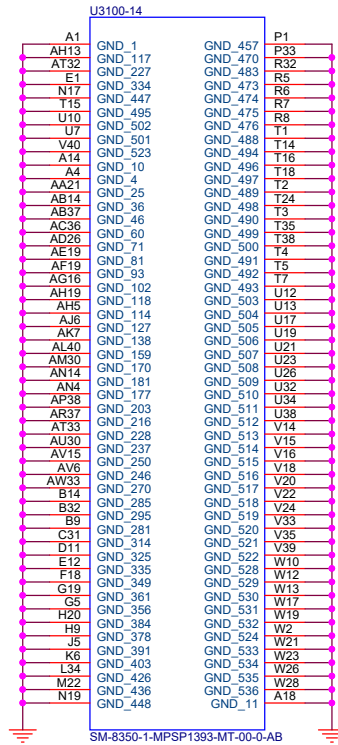
A

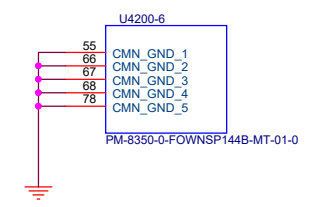
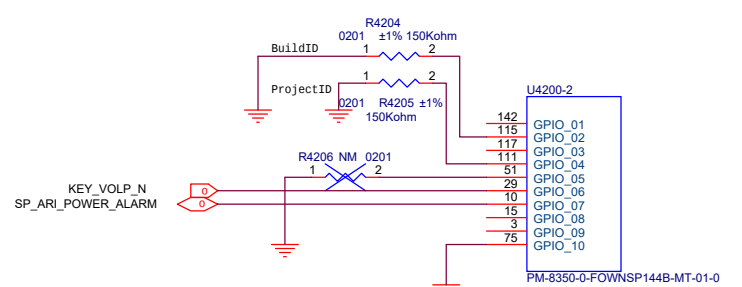
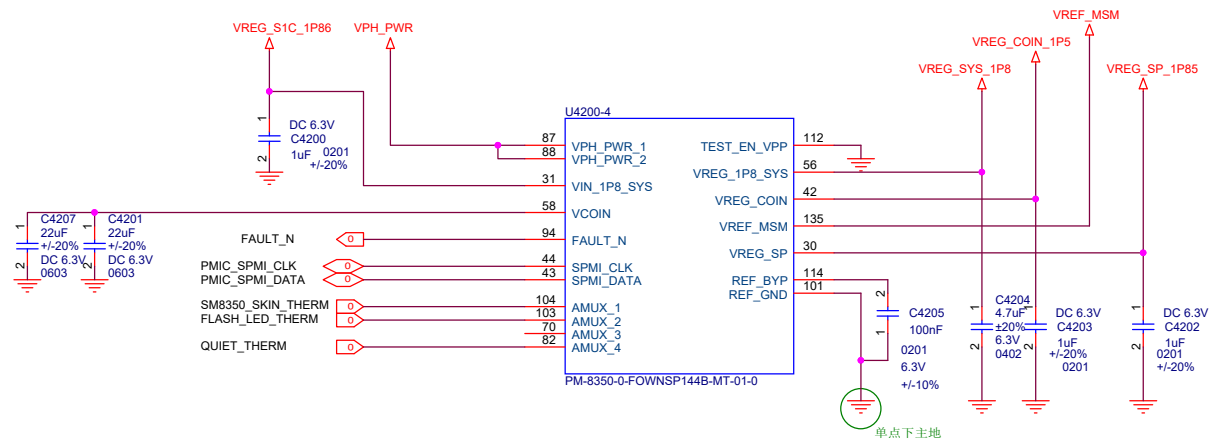
Title				
K1				
Size	Document Number Reserved(for MIPI)			
A3				Rev P0
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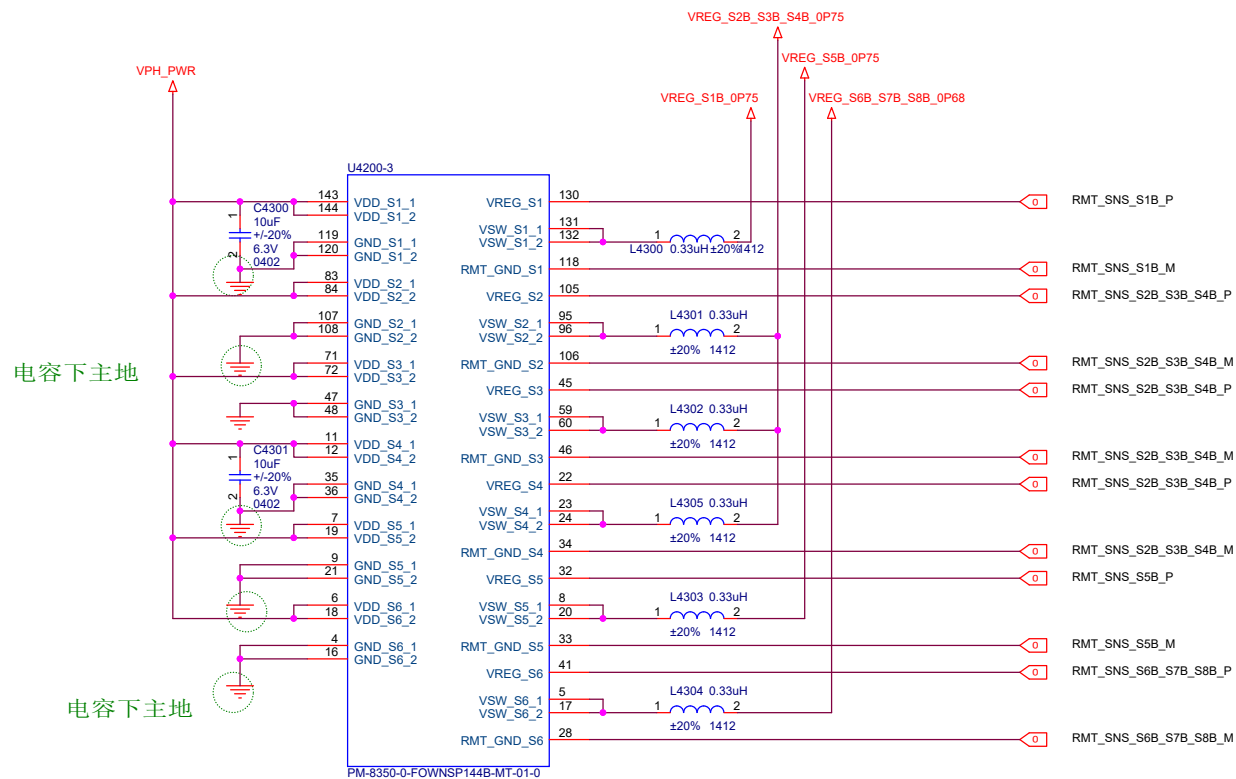












VDD_APC0 (Single Phase)

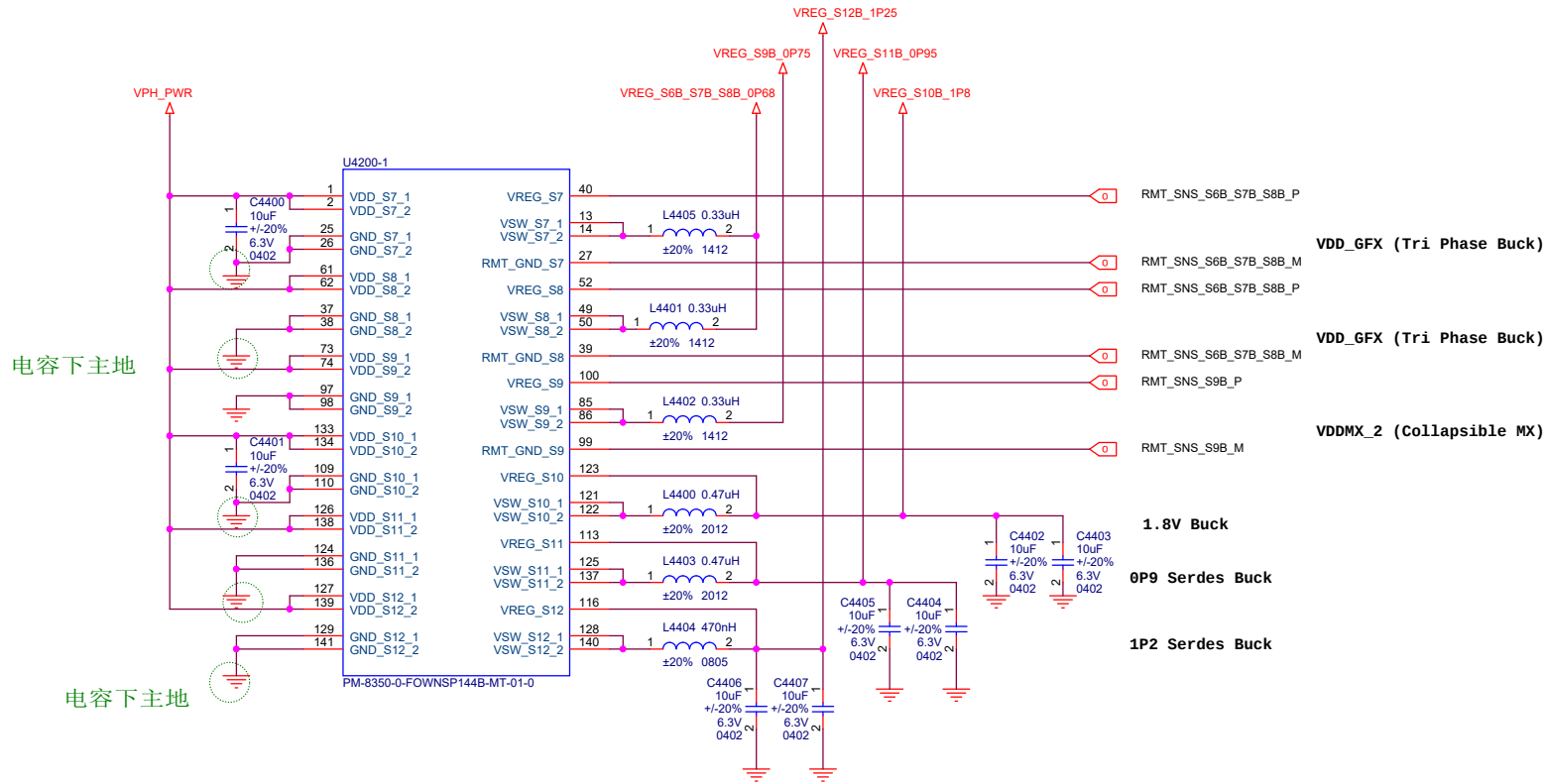
VDD_APC1(Tri Phase)

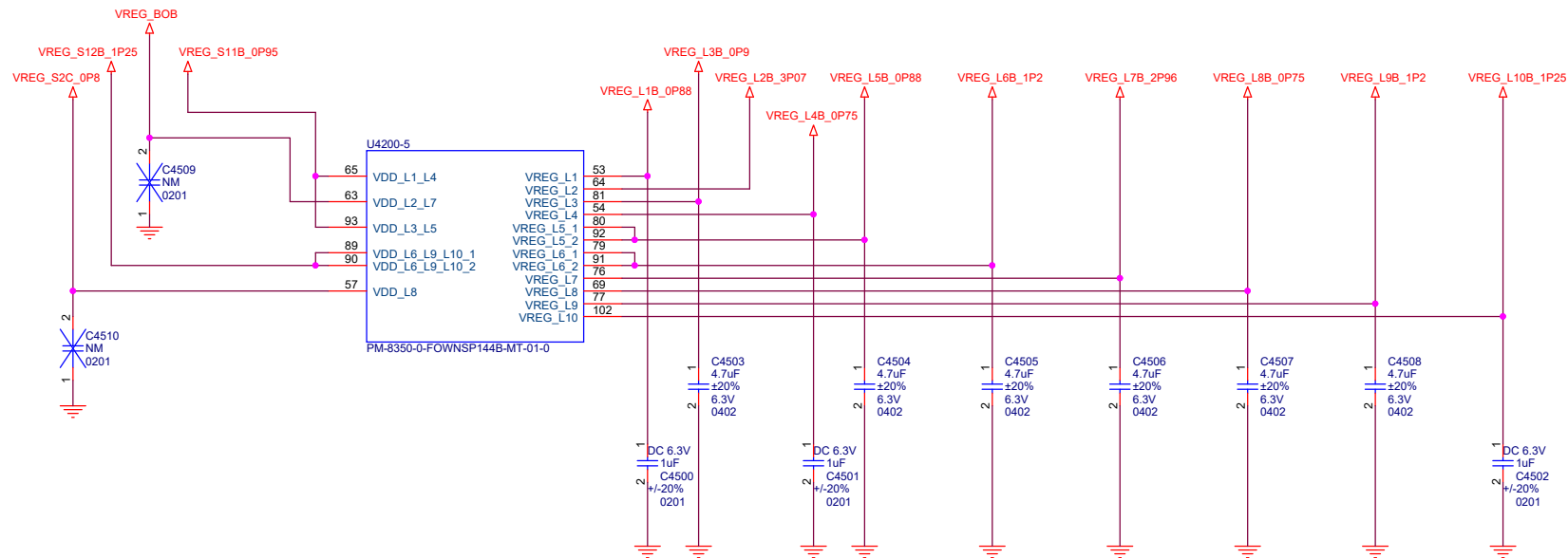
VDD_APC1(Tri Phase)

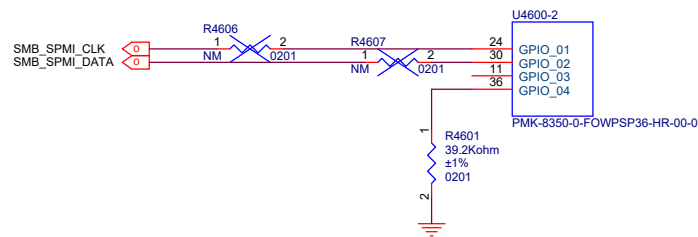
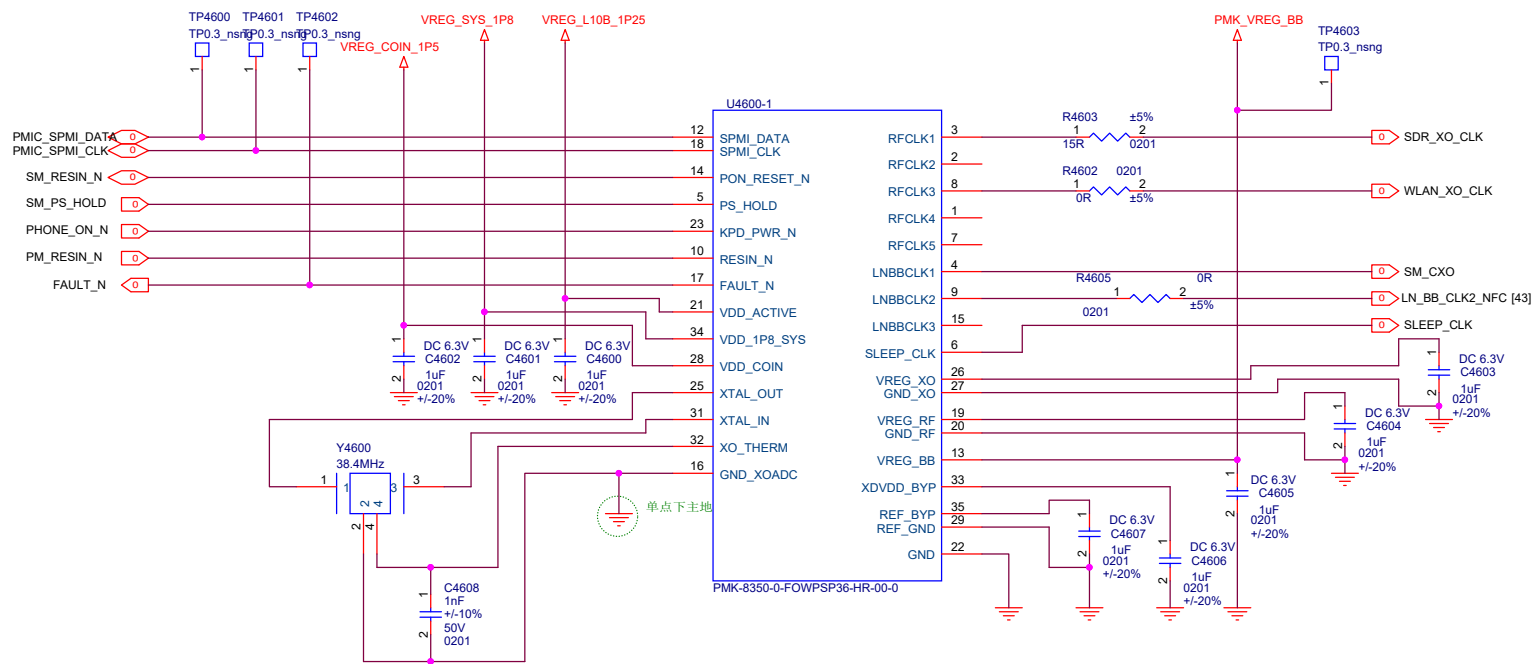
VDD_APC1(Tri Phase)

VDDMX_1 (Always On VDDMX)

VDD_GFX (Tri Phase)

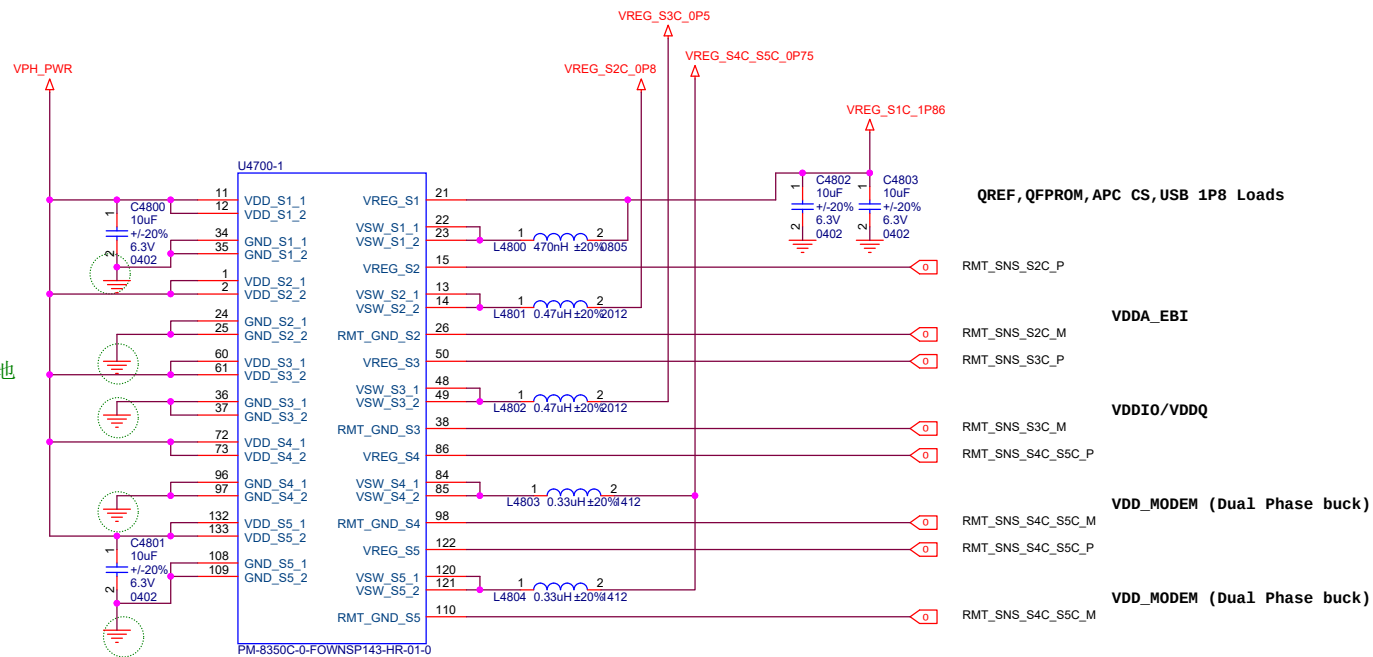


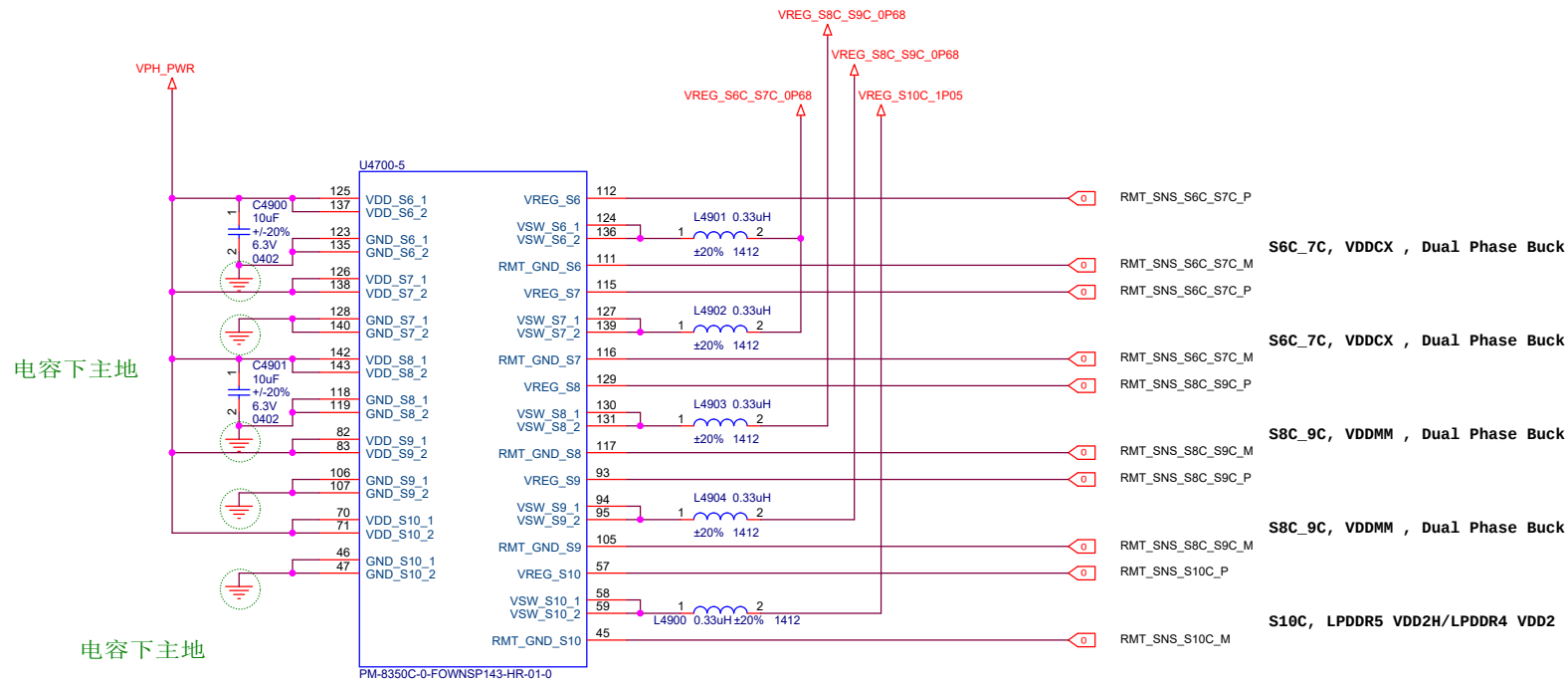


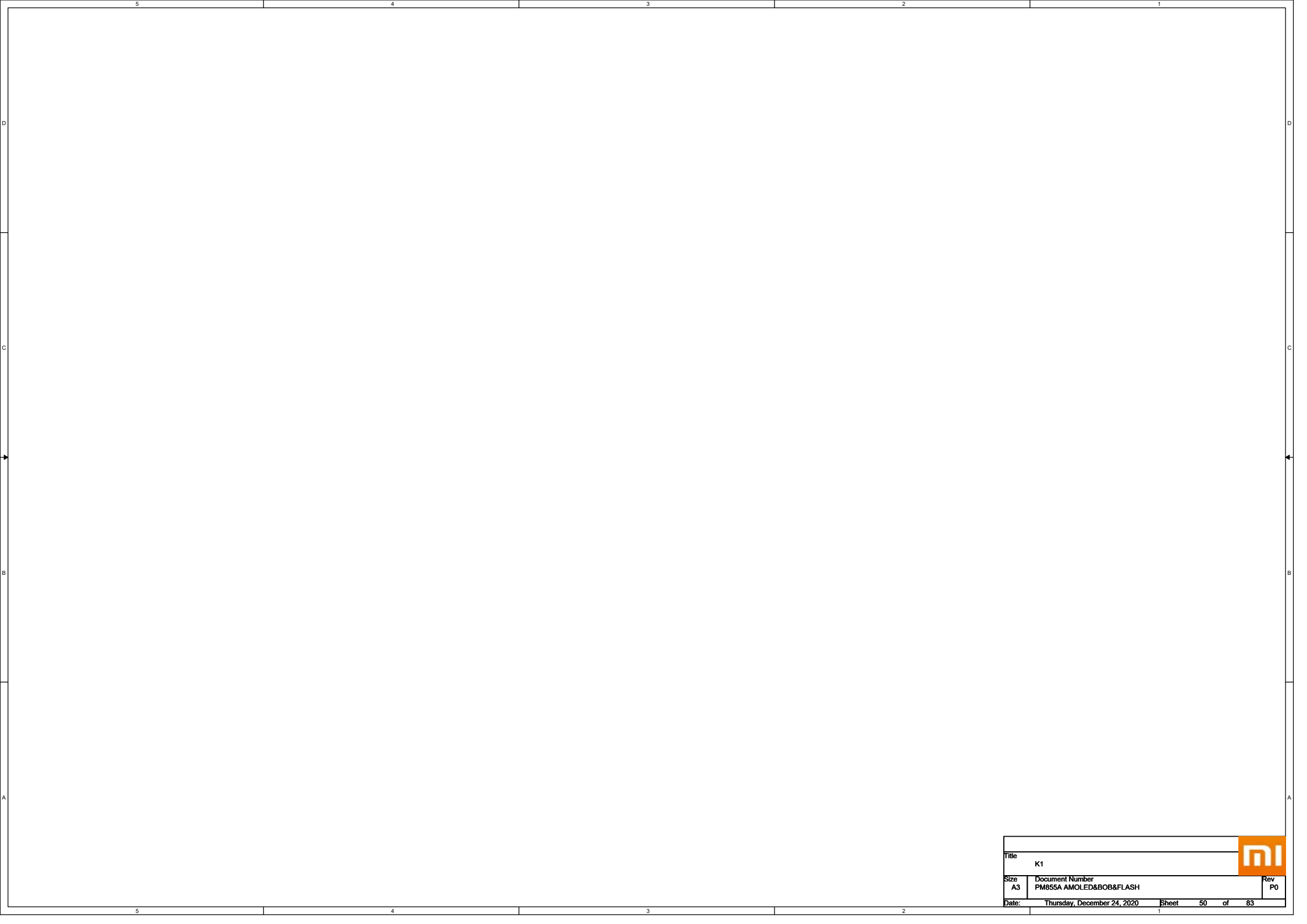


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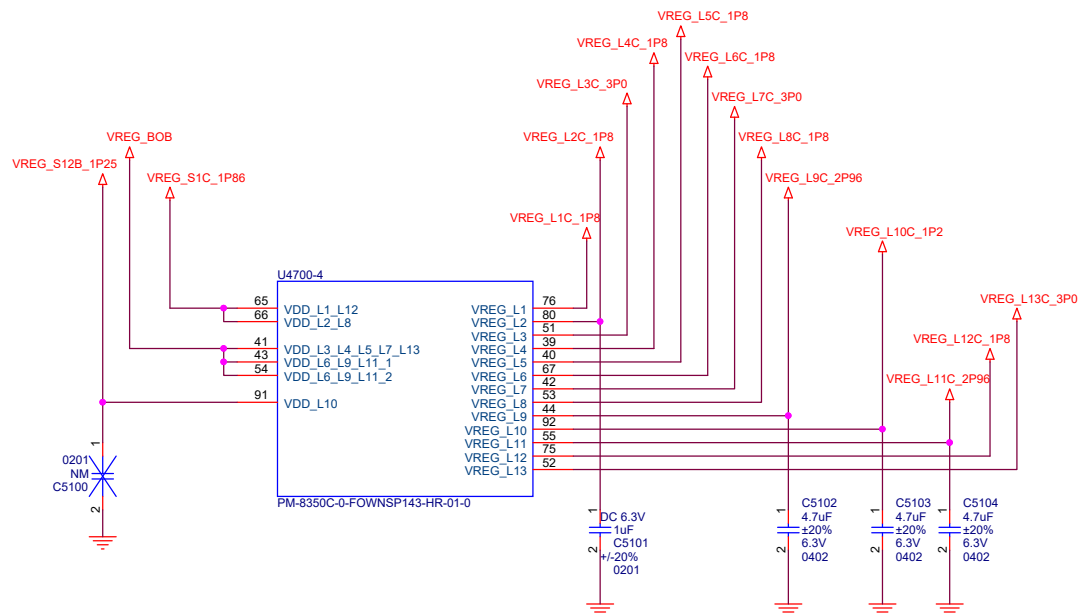


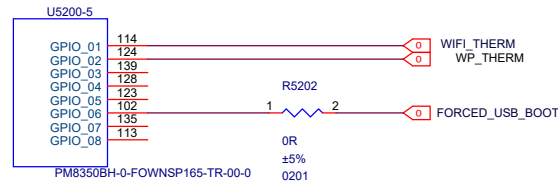
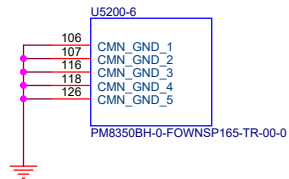
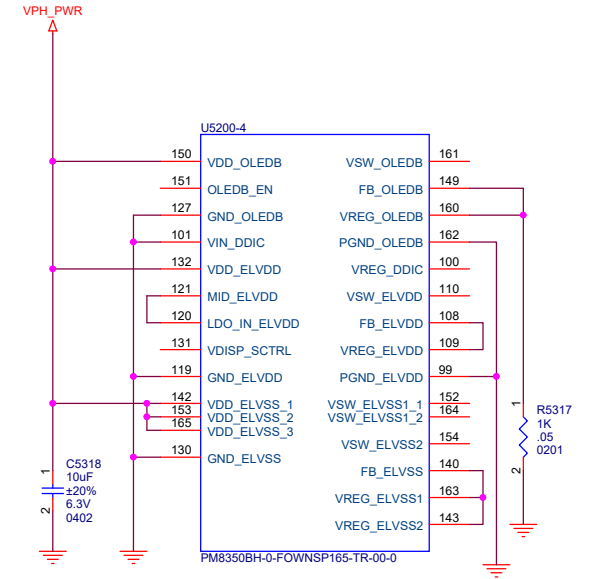
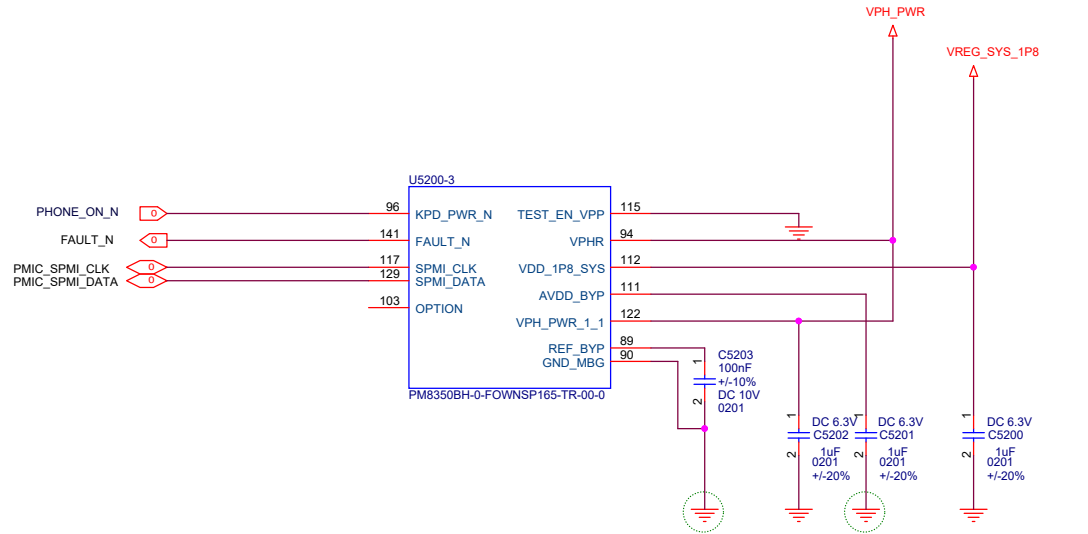


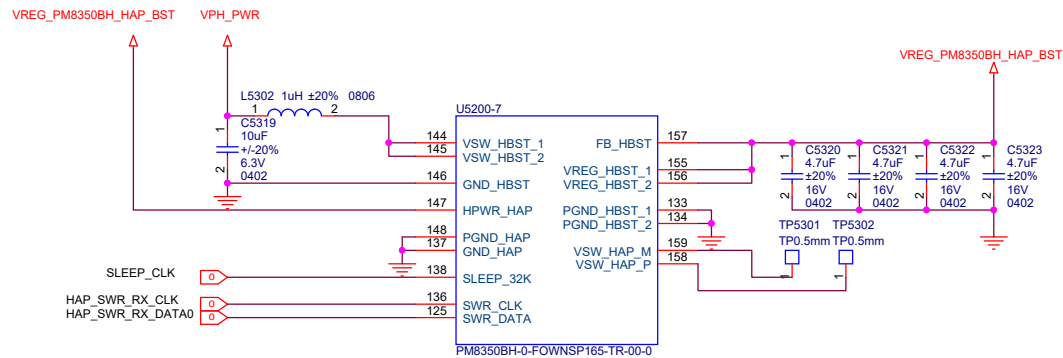
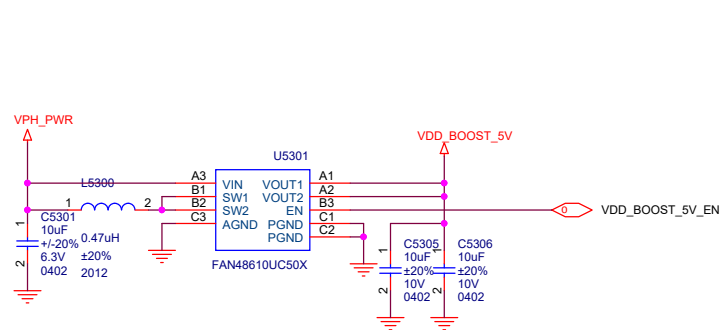


Title		K1	
Size	A3	Document Number	PM855A AMOLED&BOB&FLASH
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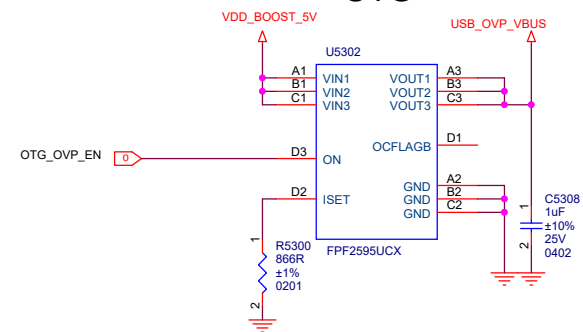


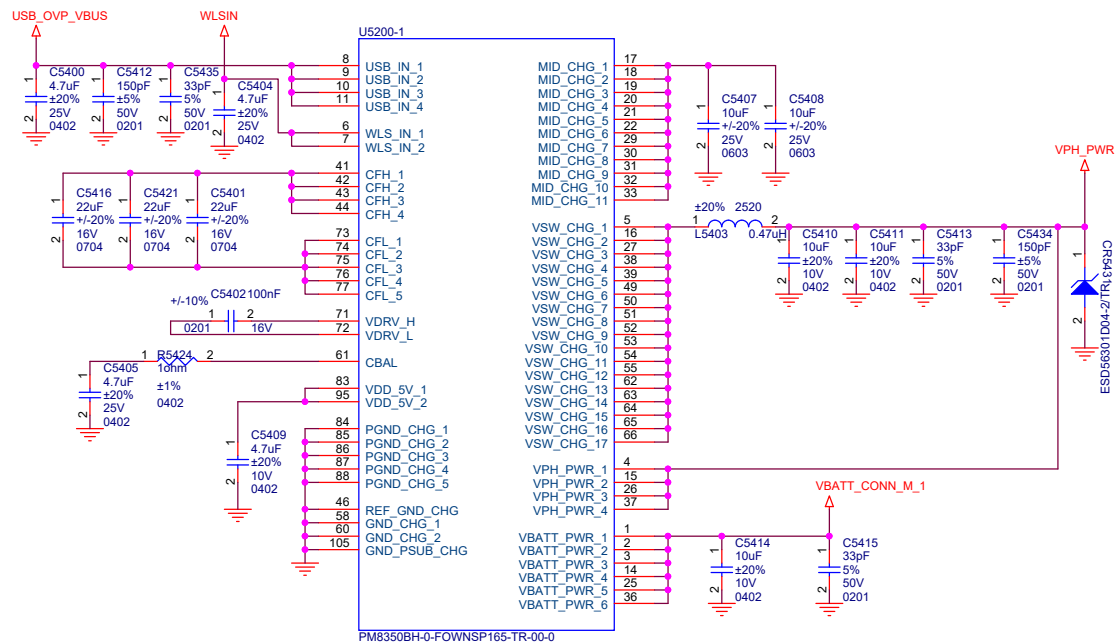




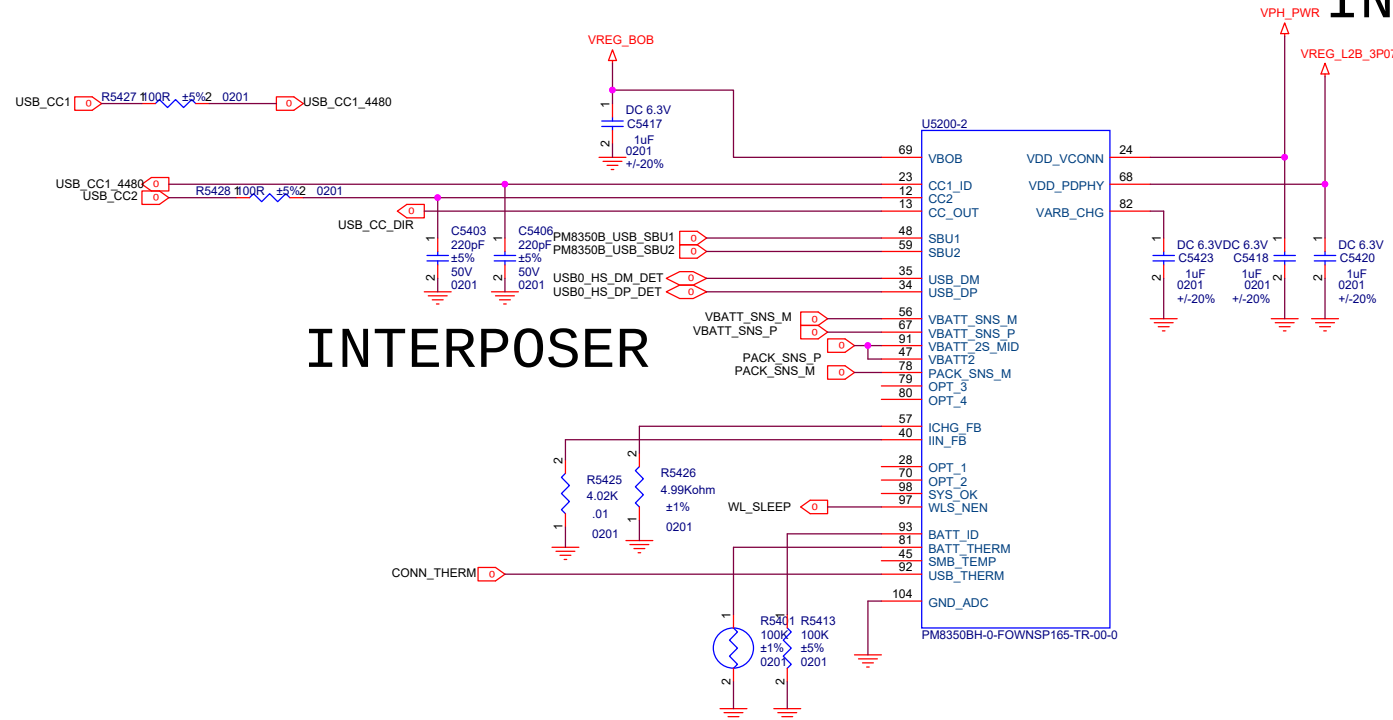


OTG



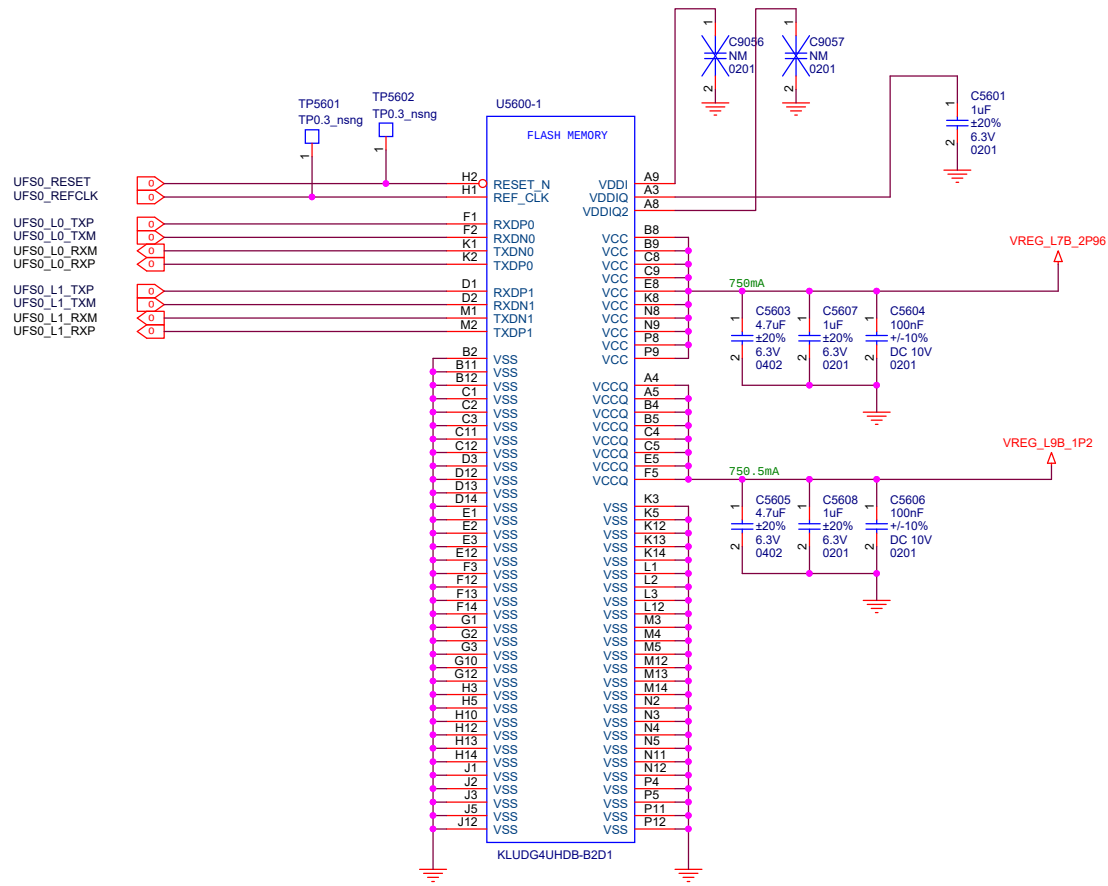


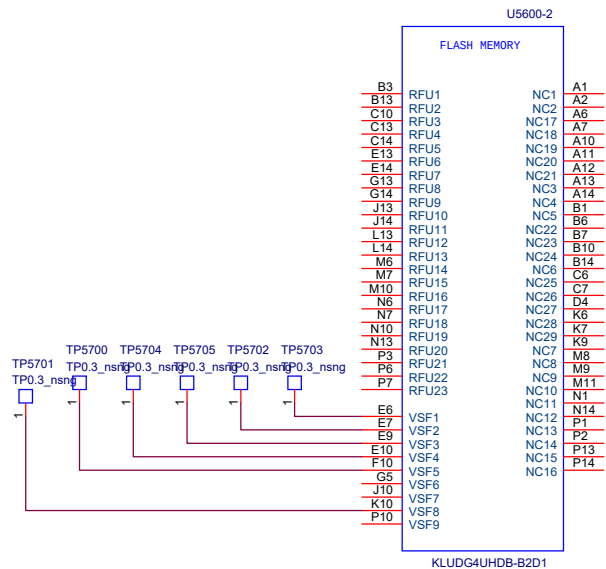
INTERPOSER

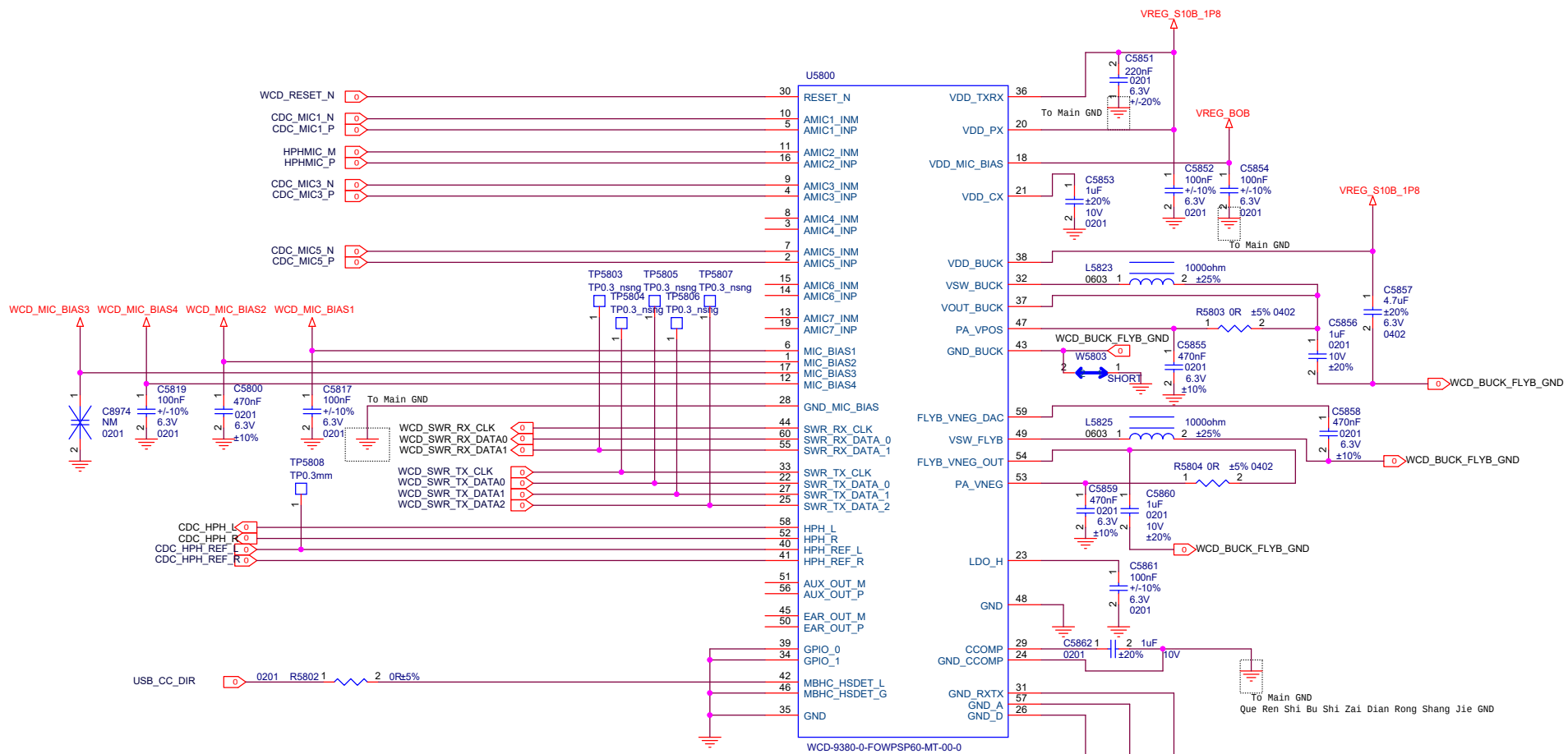


INTERPOSER

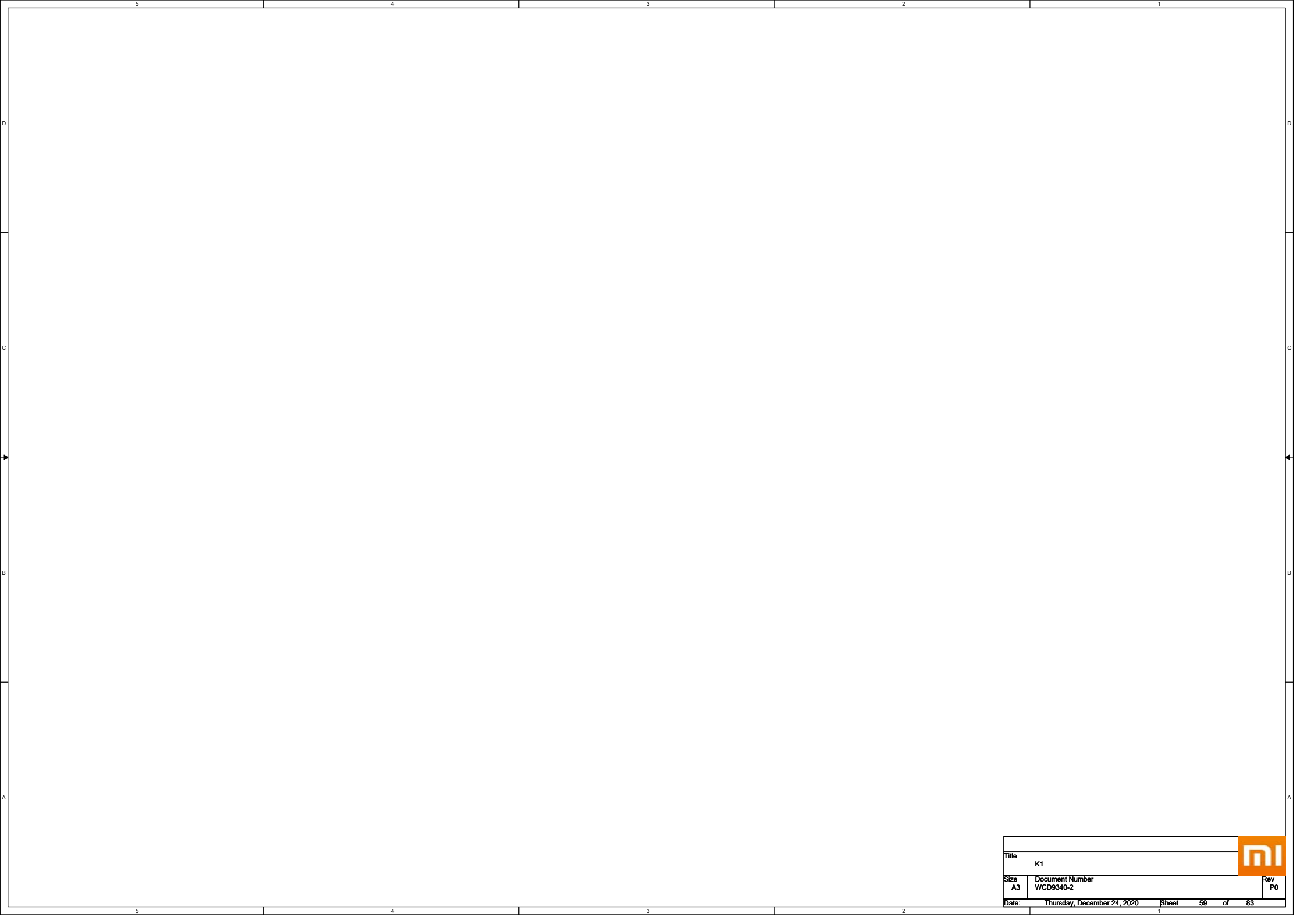
				
Title				
K1				
Size	Document Number			Rev
A3	PM855B Charger			
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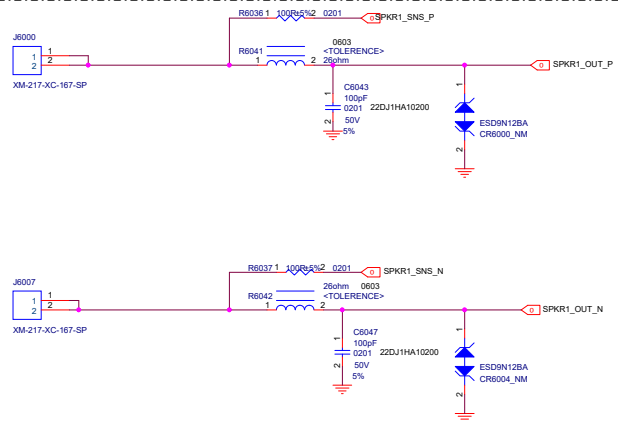
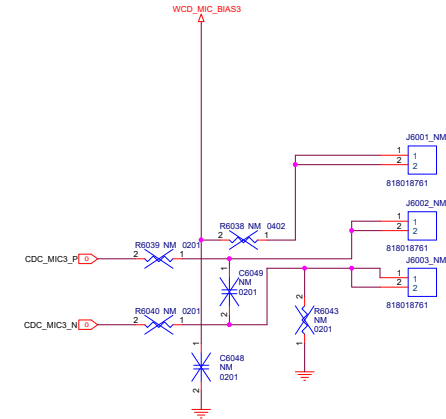
NOTE 1: ROUTE AN ISOLATED GROUND TRACE FROM CCOMP_REF TO THE CAP GROUND, THEN VIA TO THE MAIN GROUND PLANE
NOTE 2: PLACE VDD_BUCK CAP AS CLOSE TO CHIP EDGE AS POSSIBLE AND MINIMIZE LOOP INDUCTANCE
NOTE 3: USE SHORT AND WIDE TRACES FOR BUCK AND FLYBACK RELATED NETS
NOTE 4: ANALOG MIC INPUTS SHOULD BE ISOLATED WITH GROUND FROM NOISY OR SWITCHING SIGNALS AND SUPPLIES



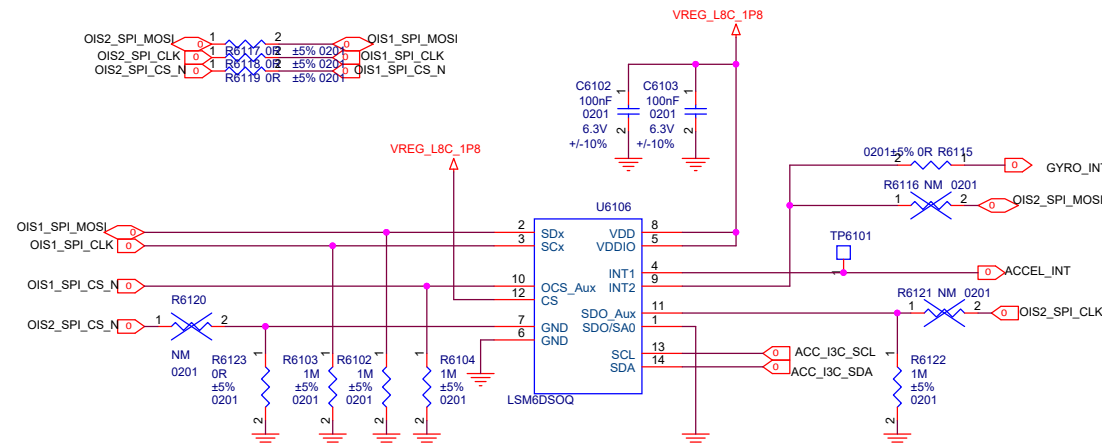
Title				
K1				
Size	Document Number			
A3	WCD9340-2			P0
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The schematic diagram illustrates the HEADSET_P and HEADSET_MIC_P_S interface circuit. The circuit is a differential signal path. On the left, two input lines are labeled HPHMIC_P (red) and HPHMIC_M (blue). These lines connect to a network of resistors and capacitors. Resistor R6029 (2.2K, ±1%, 0201) is connected to the HPHMIC_P line. Resistor R6007 (2.2K, ±1%, 0201) is connected to the HPHMIC_M line. Resistor R6030 (0K, ±5%, 0201) is connected to the HPHMIC_P line. Resistor R6005 (0K, ±5%, 0201) is connected to the HPHMIC_M line. Capacitor C6010 (NM, 0201) is connected to the HPHMIC_P line. Capacitor C6005 (NM, 0201) is connected to the HPHMIC_M line. The circuit is powered by WCD_MIC_BIAS2 (red) and GND (blue). The output of the circuit is connected to HEADSET_MIC_P_S (red) via a close to type-C connection.

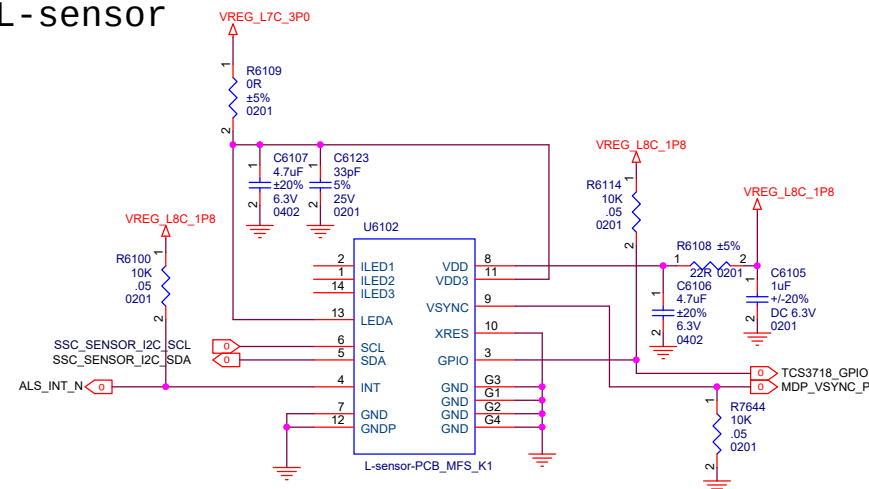
R6029、R6004、C6005靠近WCD9385（U5800）端放置。

[illegible]

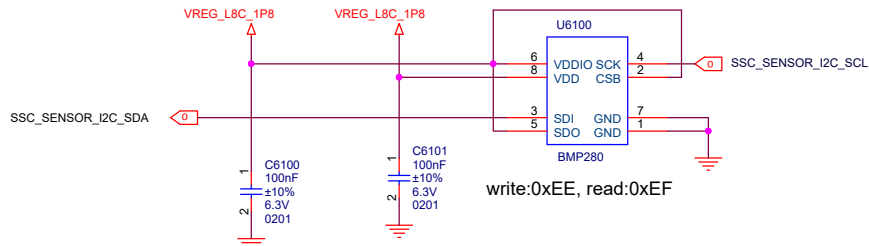
A+G



P_L-sensor



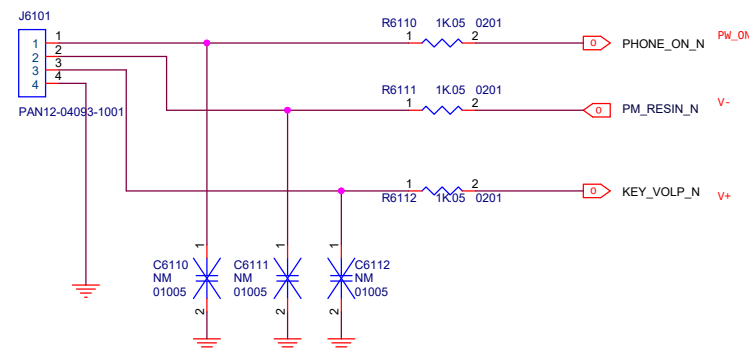
PRESSURE



FOD

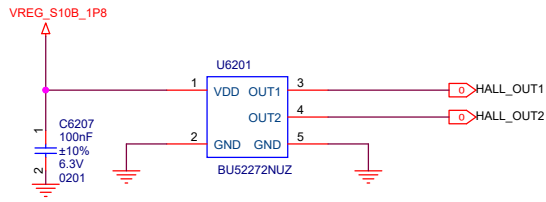
A+G MCU

Sidekey

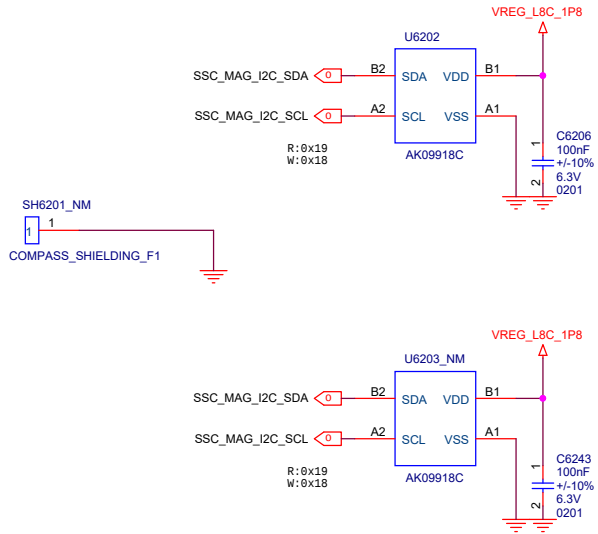


TOF&Flicker&FLASH&MIC INTERPOSER

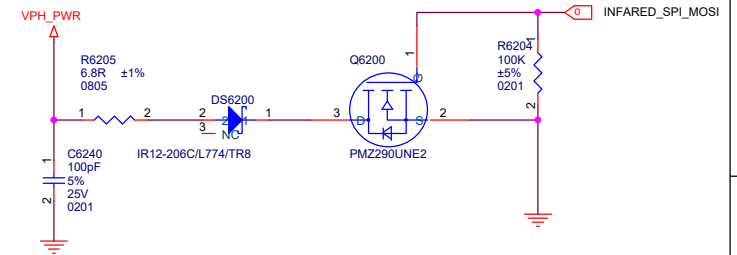
HALL



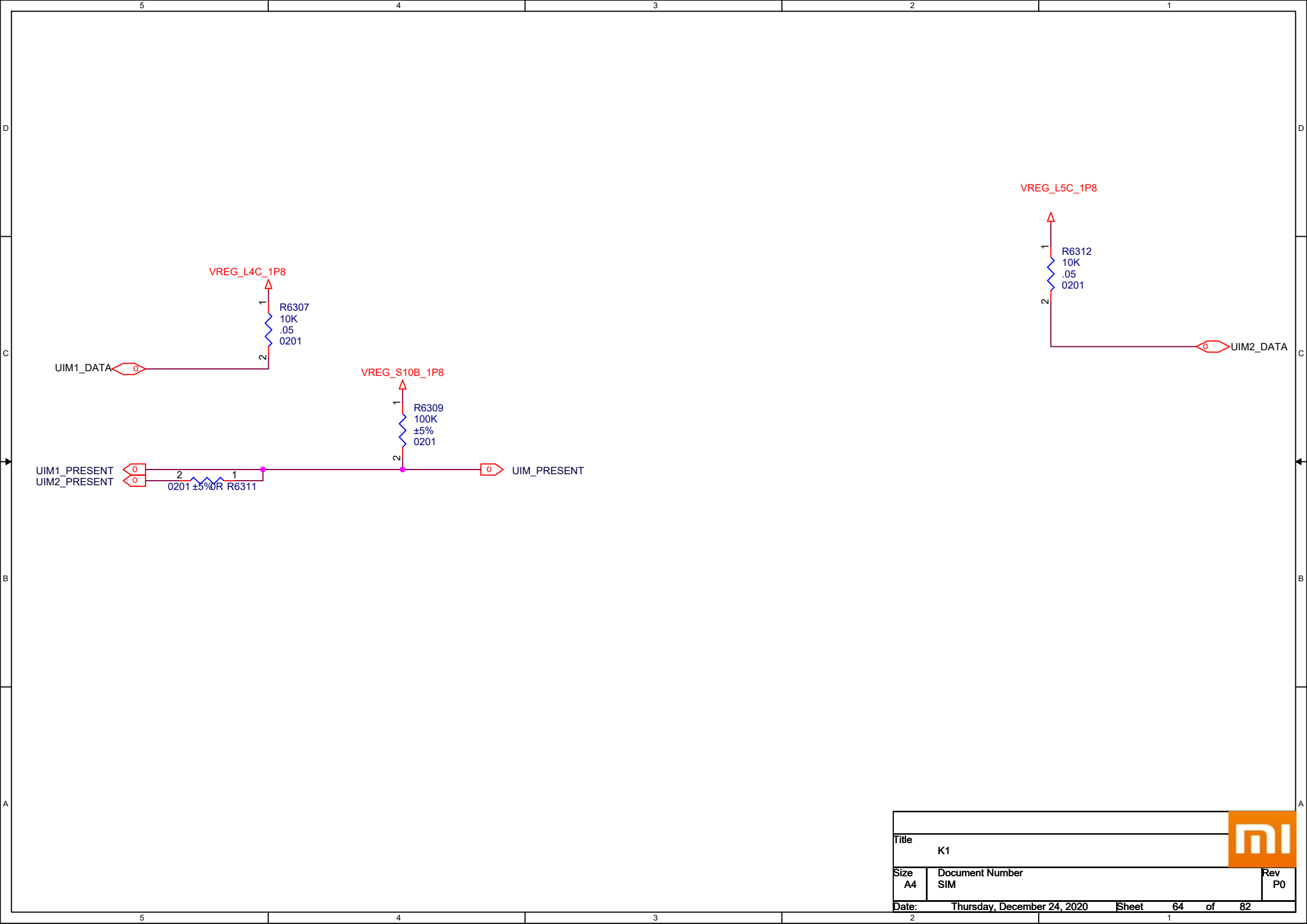
COMPASS



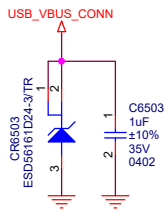
INFRARED



					
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To Main DG

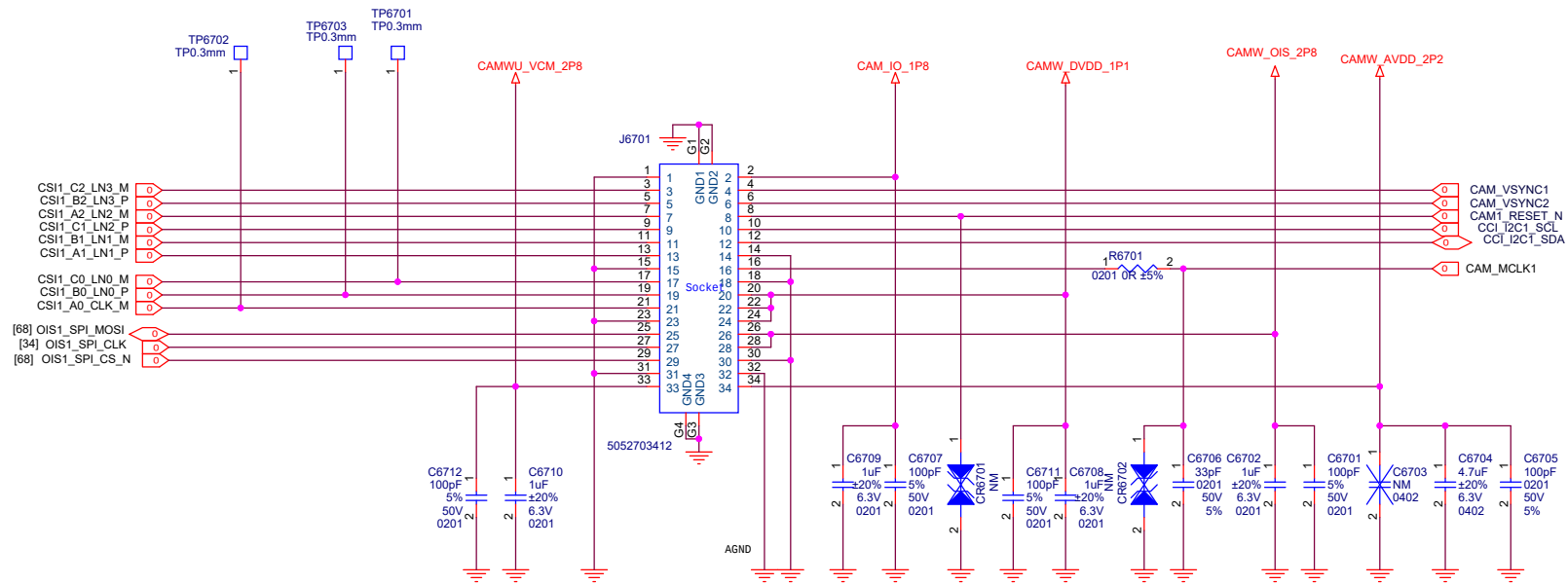




K1/K1A front 20M 3T2 DPHY

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K1			
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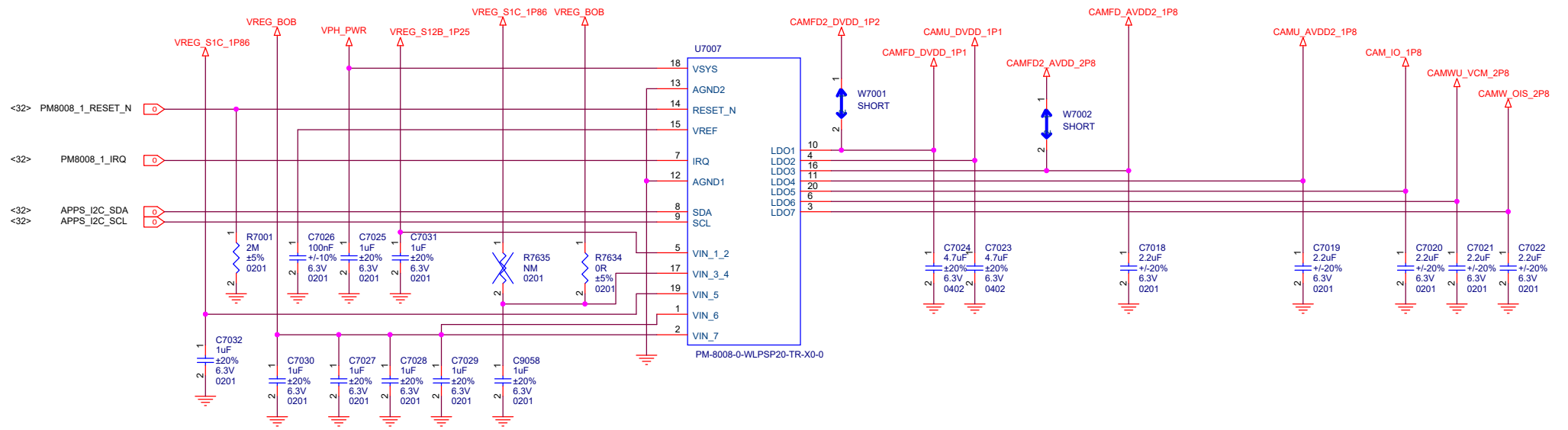
K1/K1A 50M GN2



INTERPOSER

K1 ULTRA 48M CAMERA

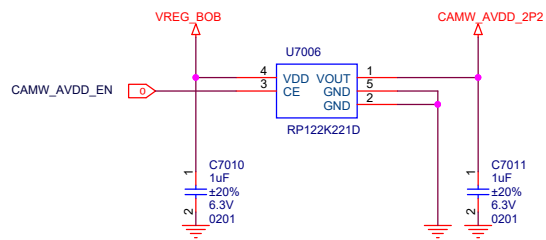
K1 FOLDED 48M CAMERA



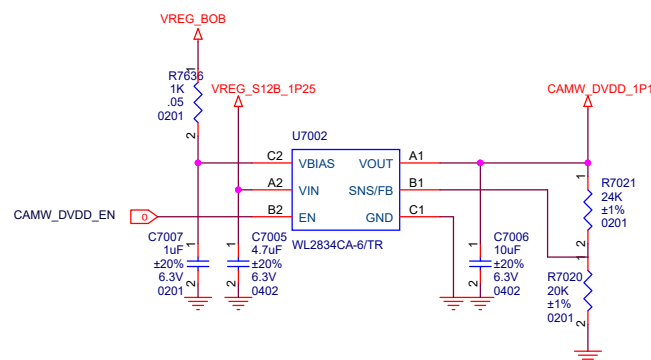
INTERPOSER

INTERPOSER

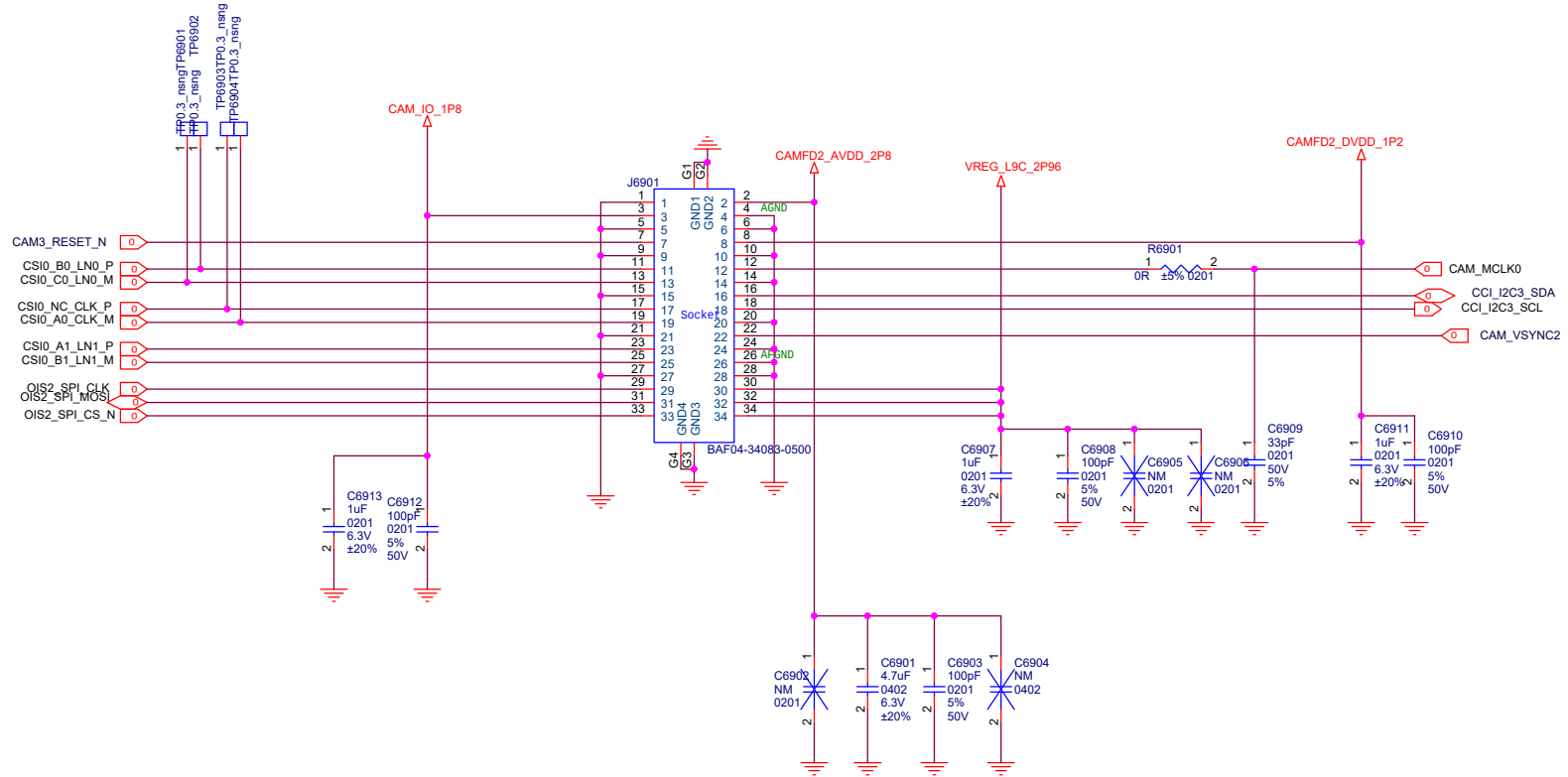
INTERPOSER



INTERPOSER

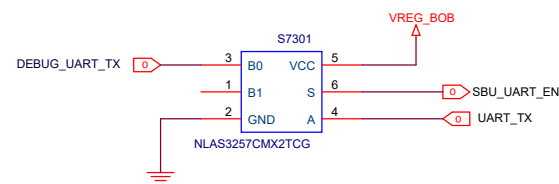
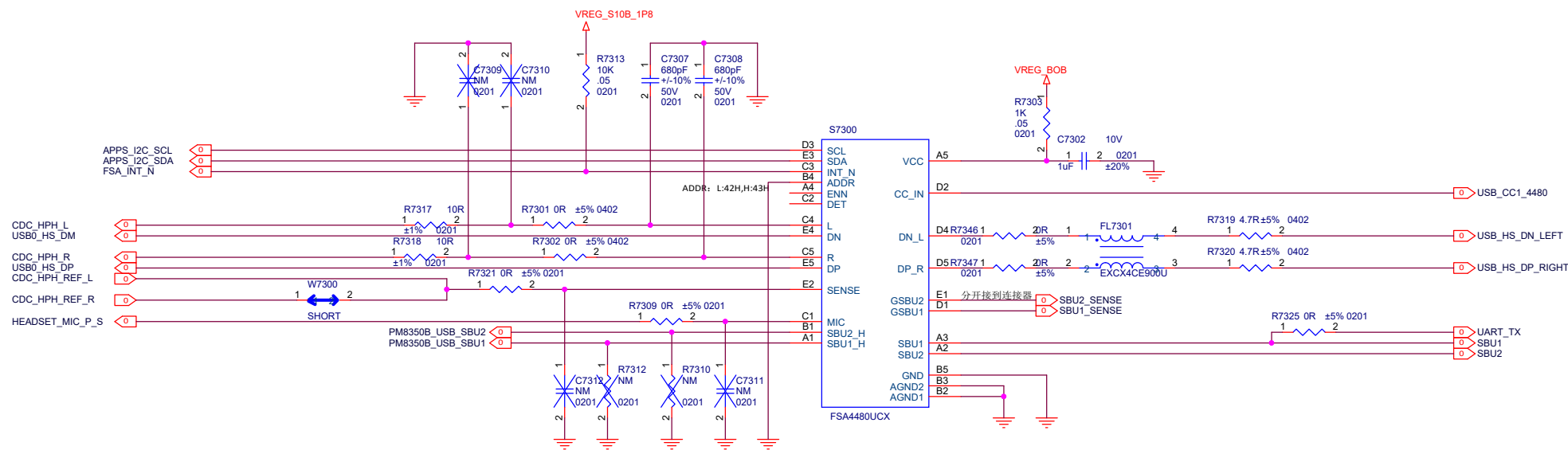


8M Folded NM 主板

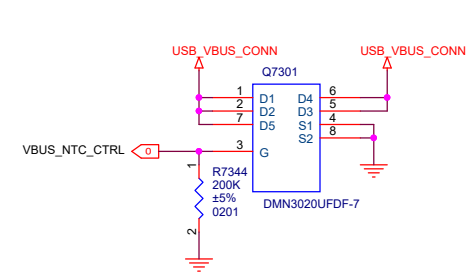




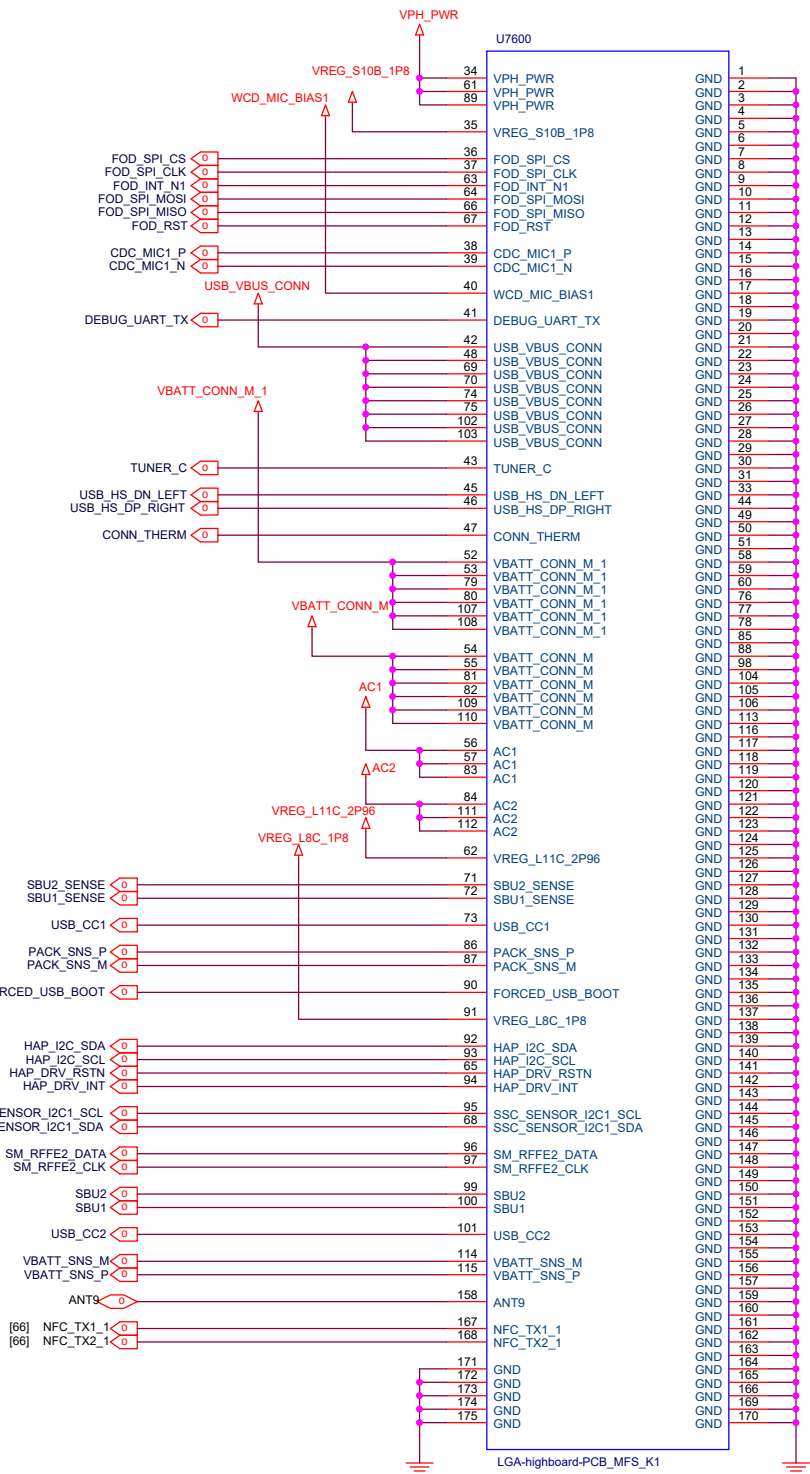


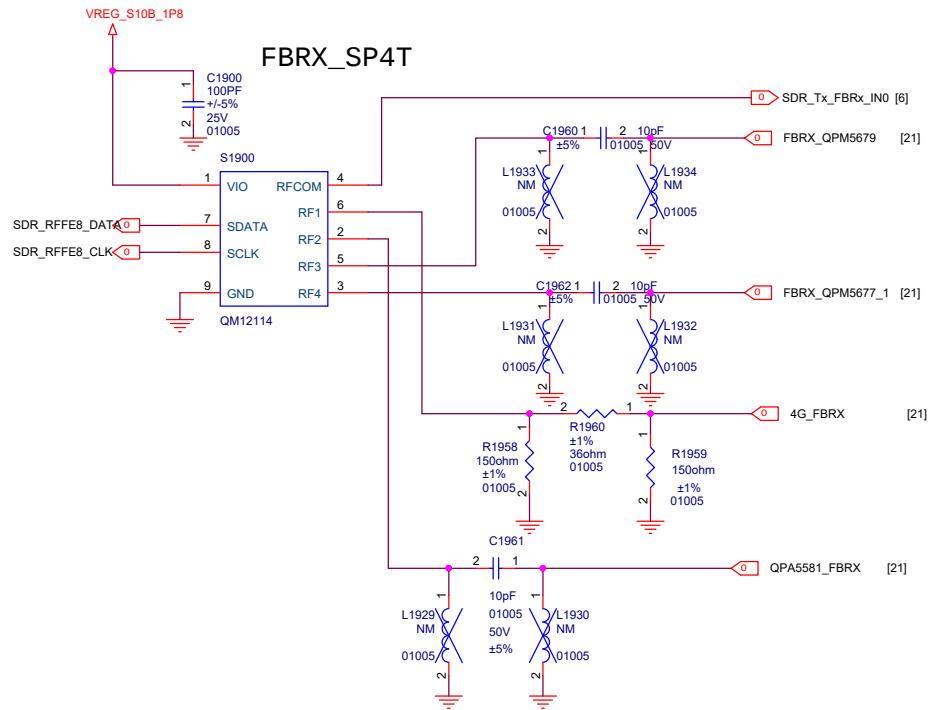


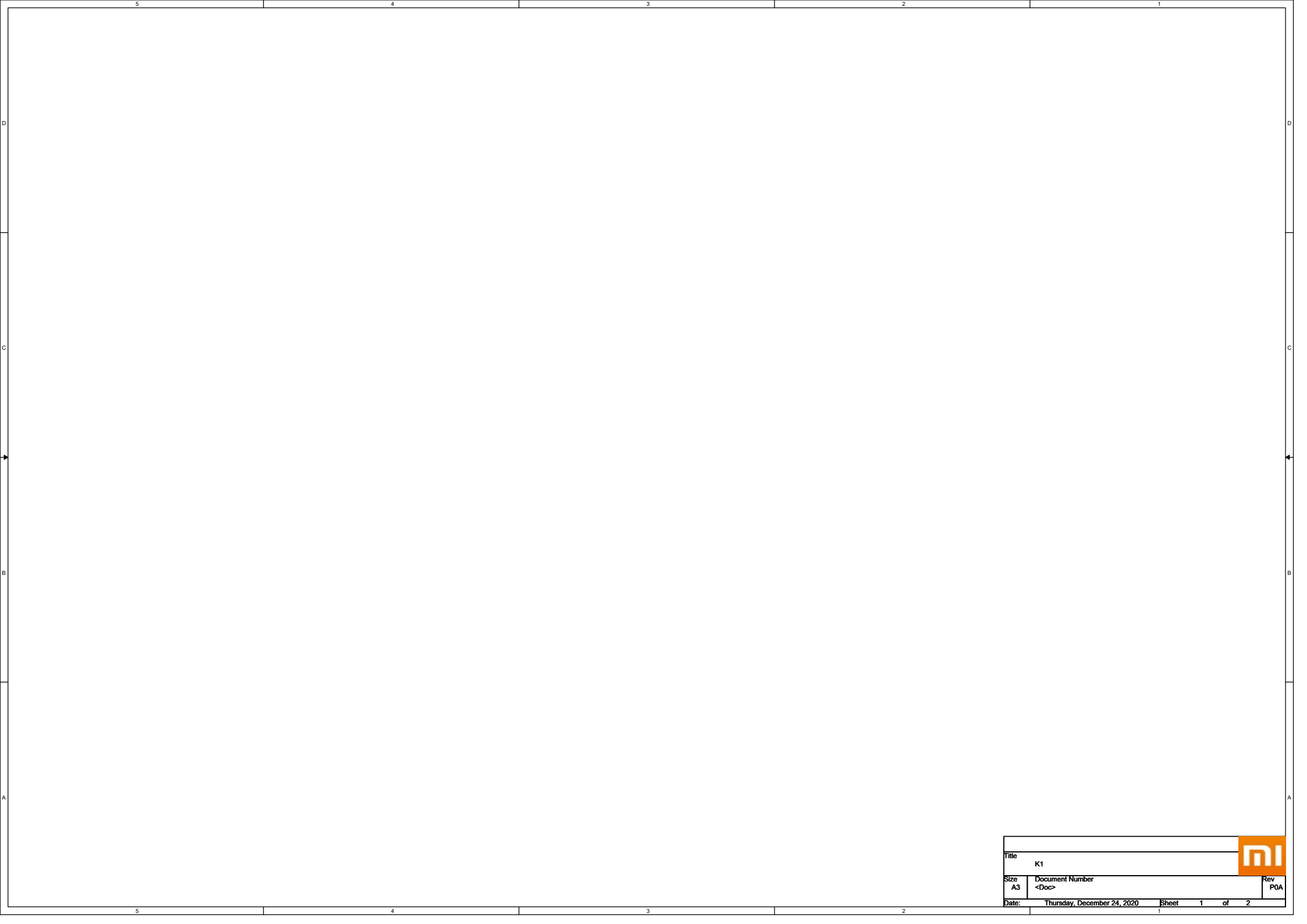
DG



Title			
K1			
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D

D

C

C

B

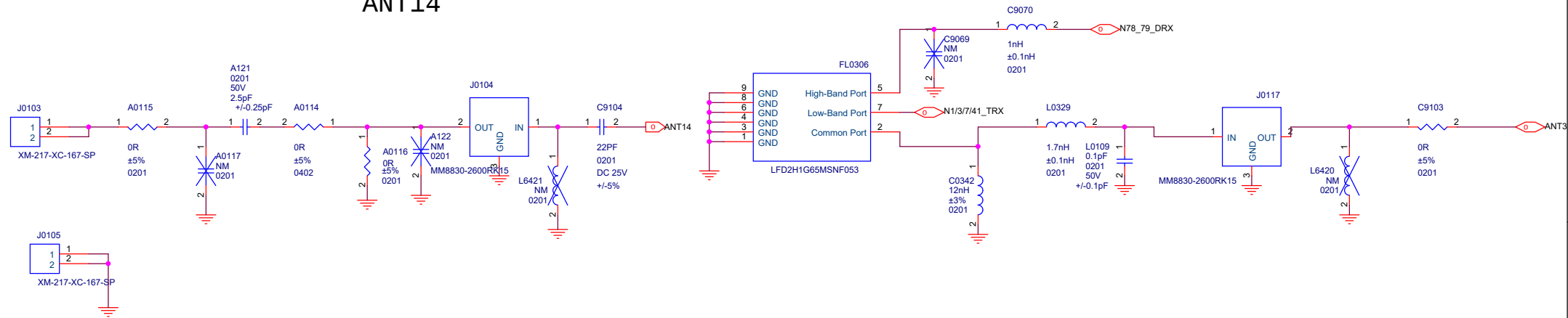
B

A

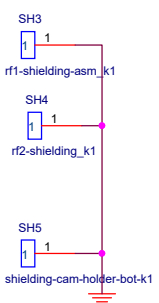
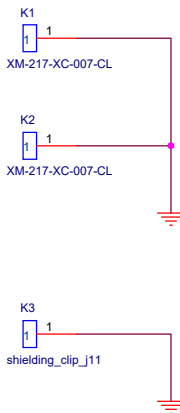
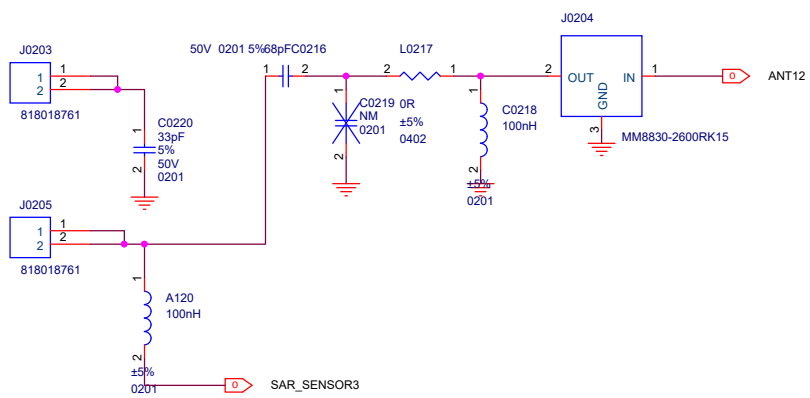
A

Title		K1			
Size	A3	Document Number	<Doc>		
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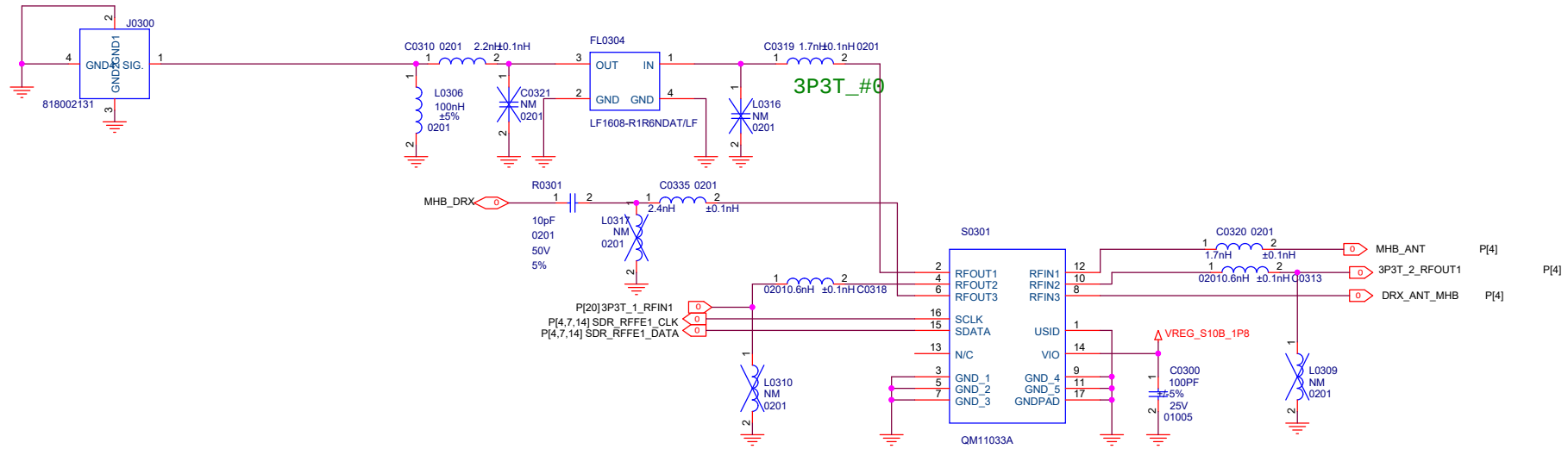
ANT14



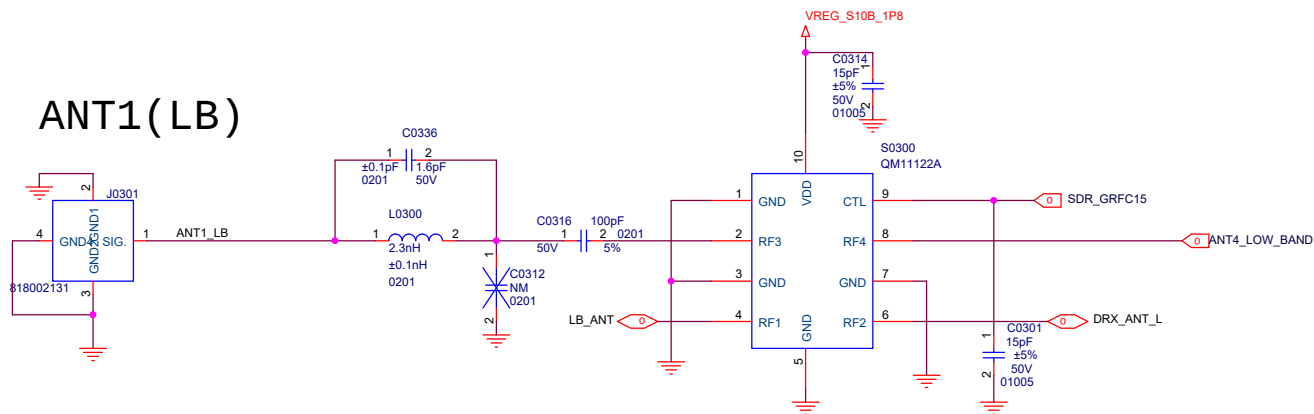
ANT12

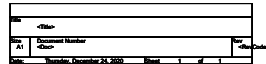


ANT2 (TRx_MHB)

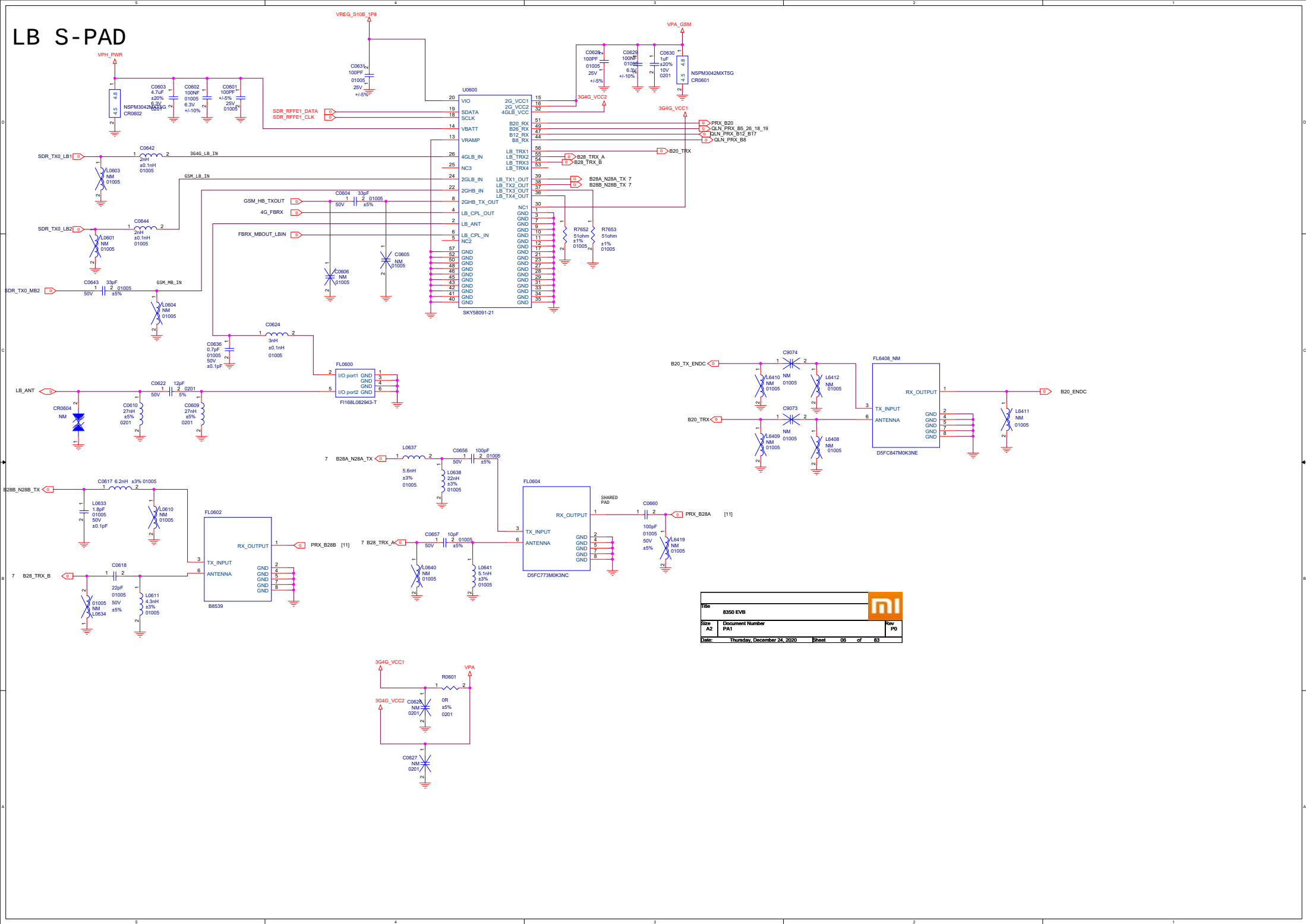


ANT1 (LB)

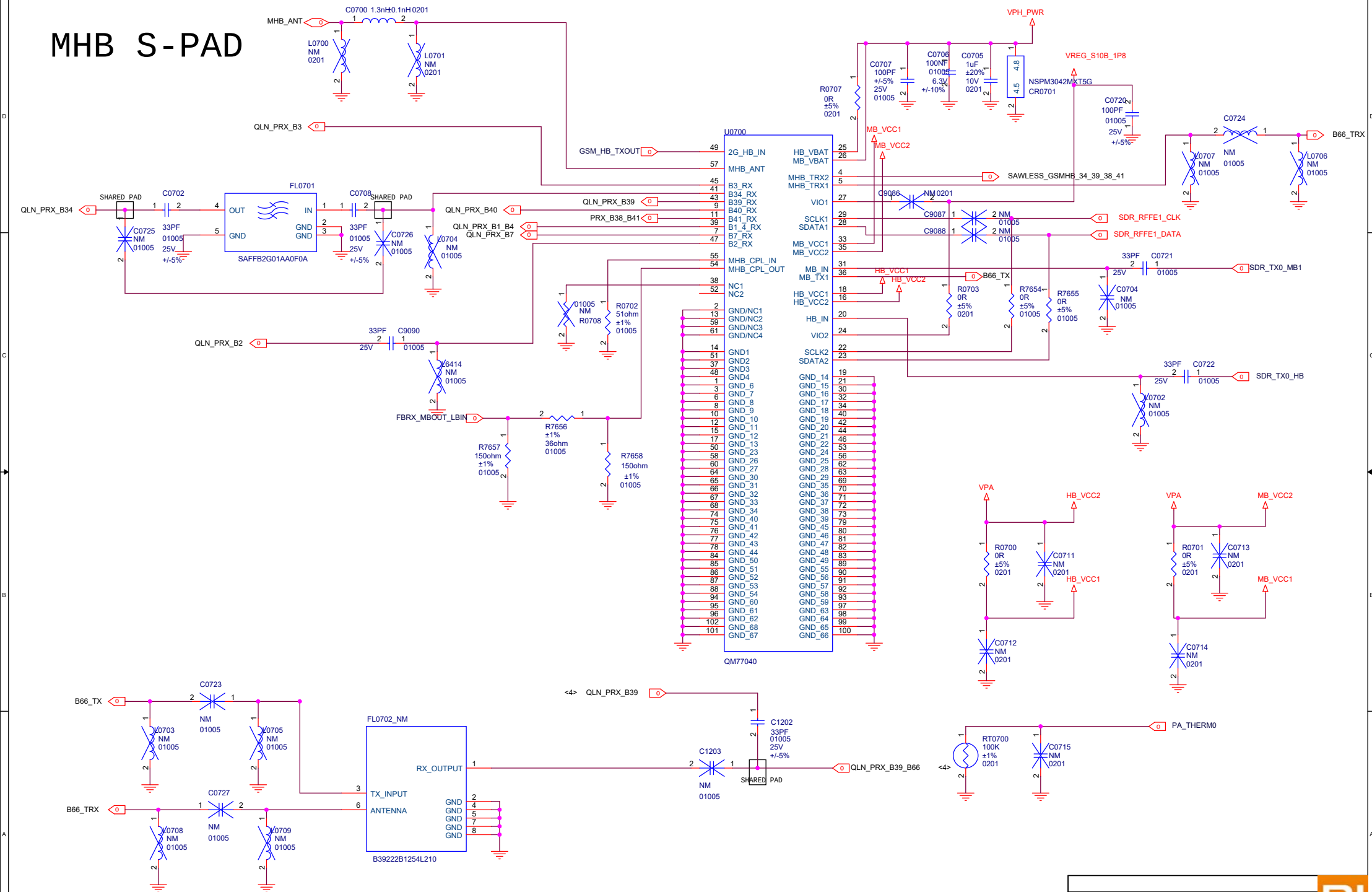




LB S-PAD

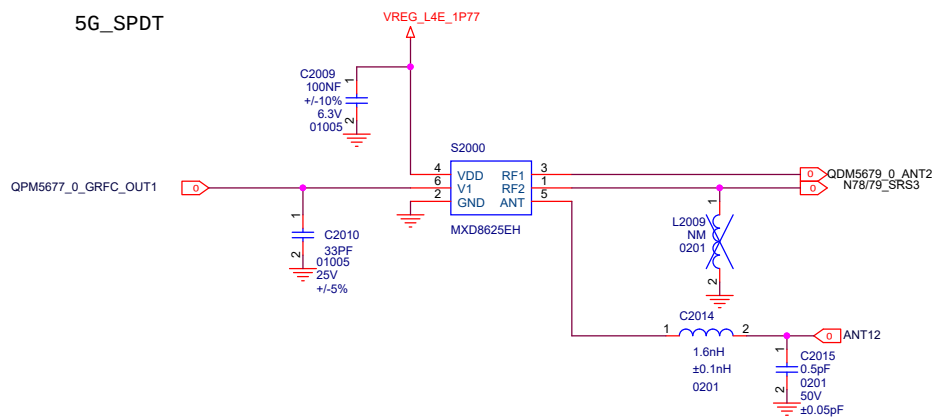


MHB S-PAD

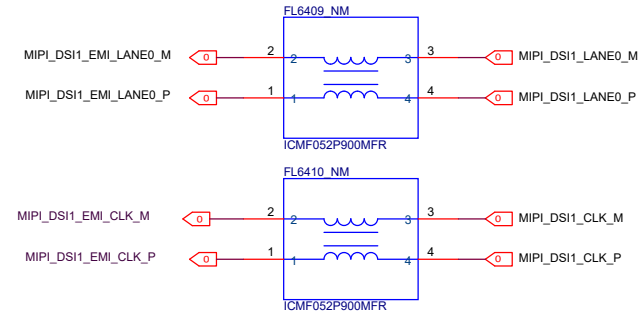
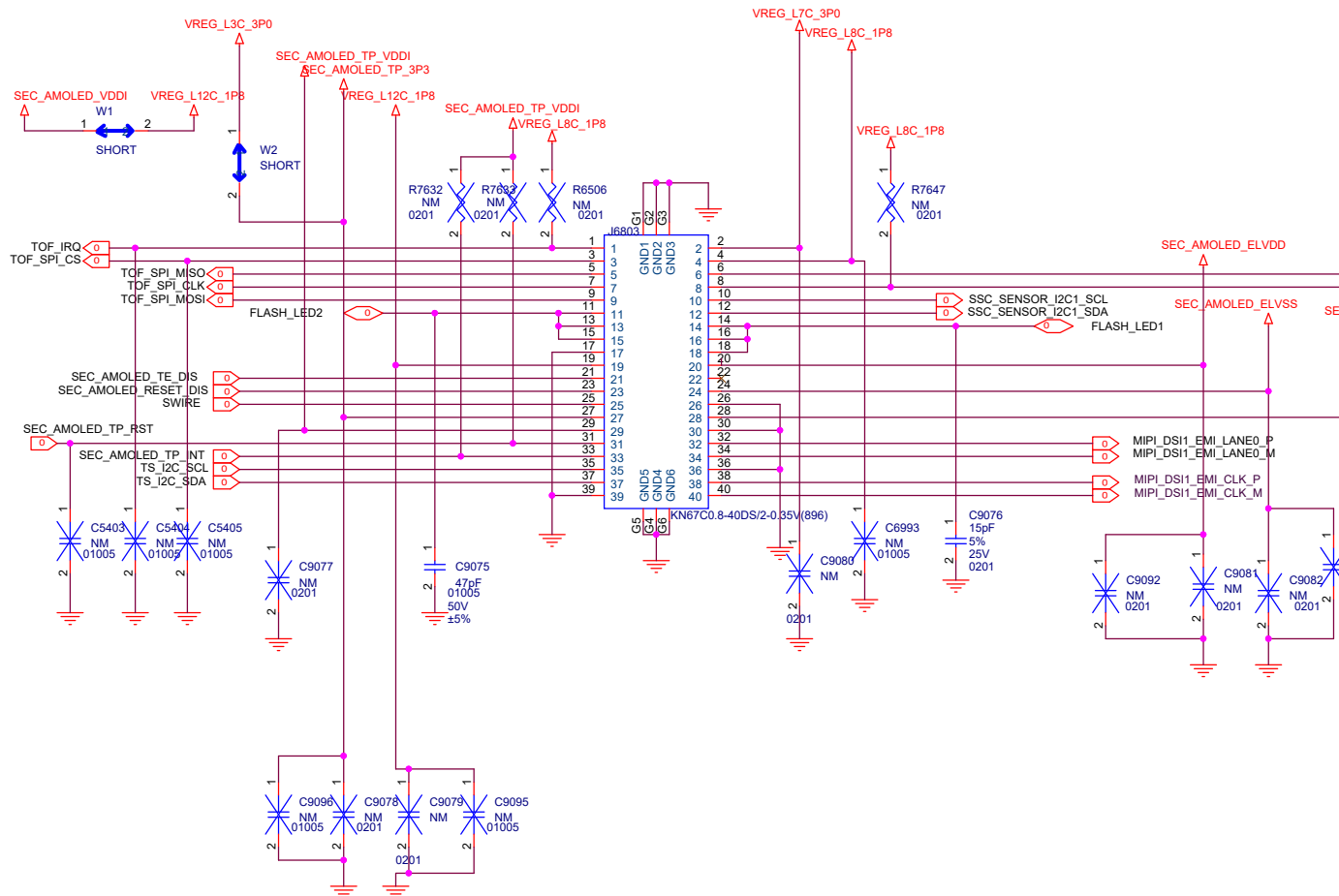


Title		8350 EVB		
Size A3	Document Number PA2		Rev P0	
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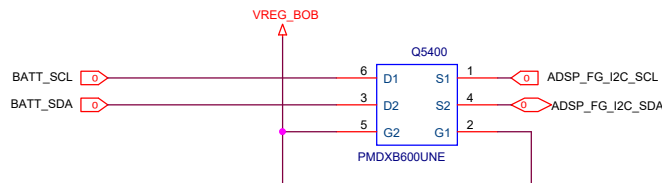
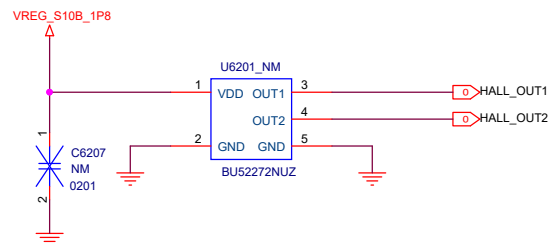
5G_SPDT



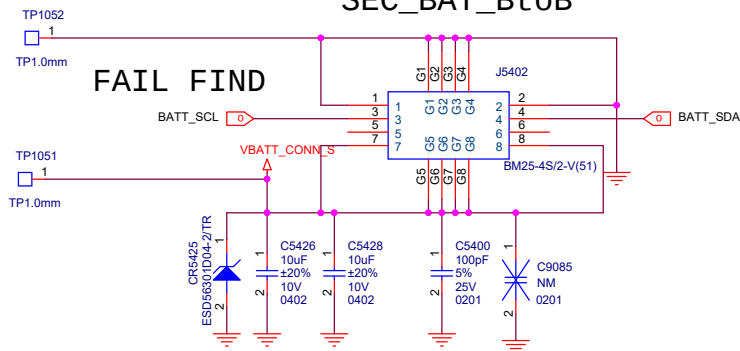
TOF&Flicker&FLASH&MIC



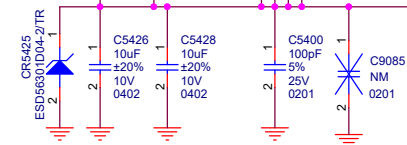
HALL



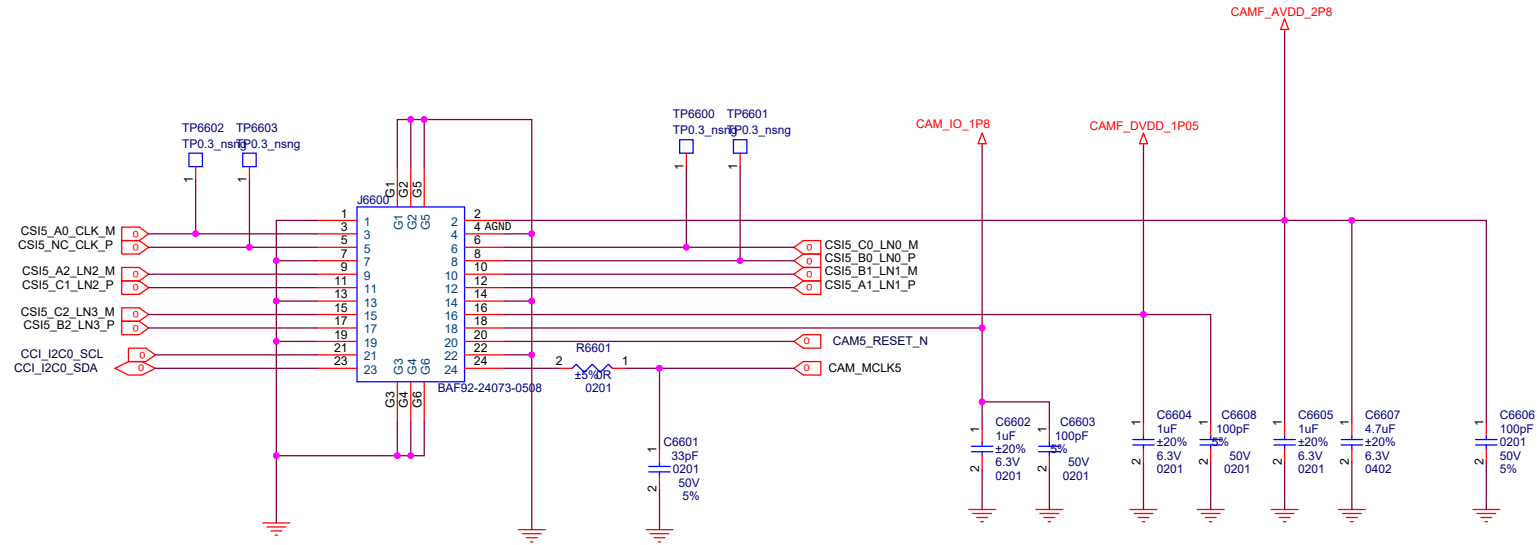
SEC_BAT_BtoB



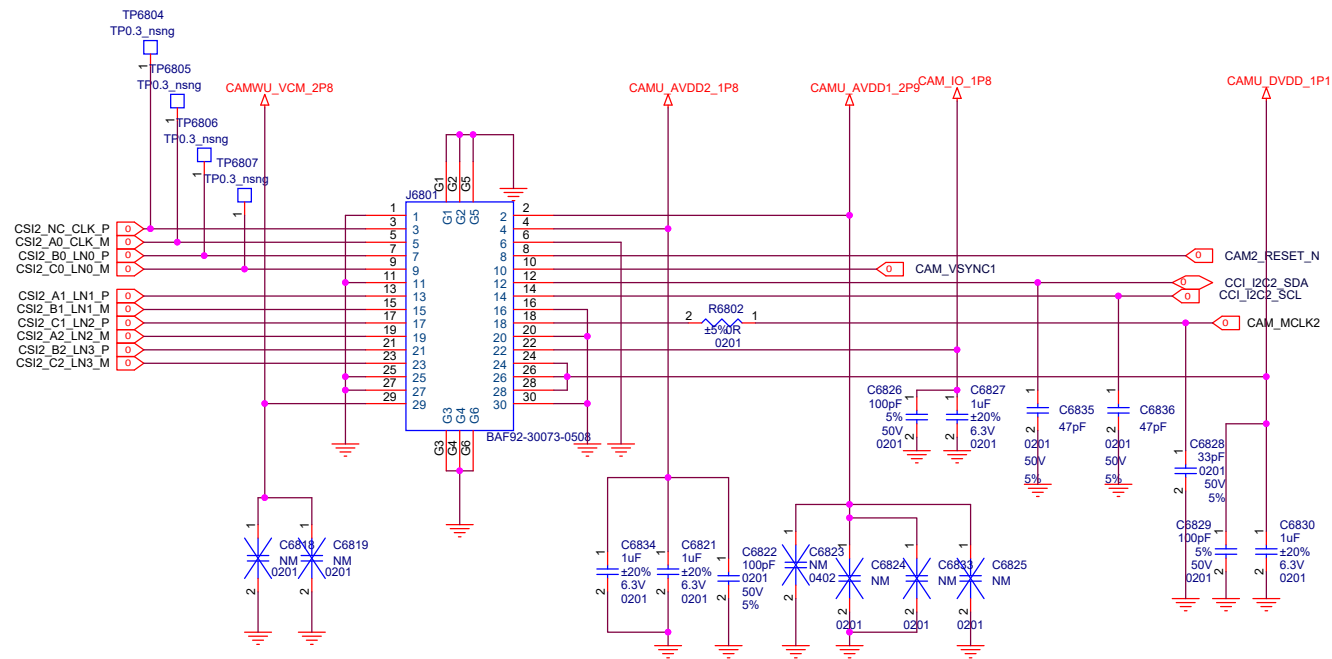
FAIL FIND



K1/K1A front 20M 3T2 DPHY



K1 ULTRA 48M CAMERA



K1 FOLDED 48M CAMERA

